

10/019116

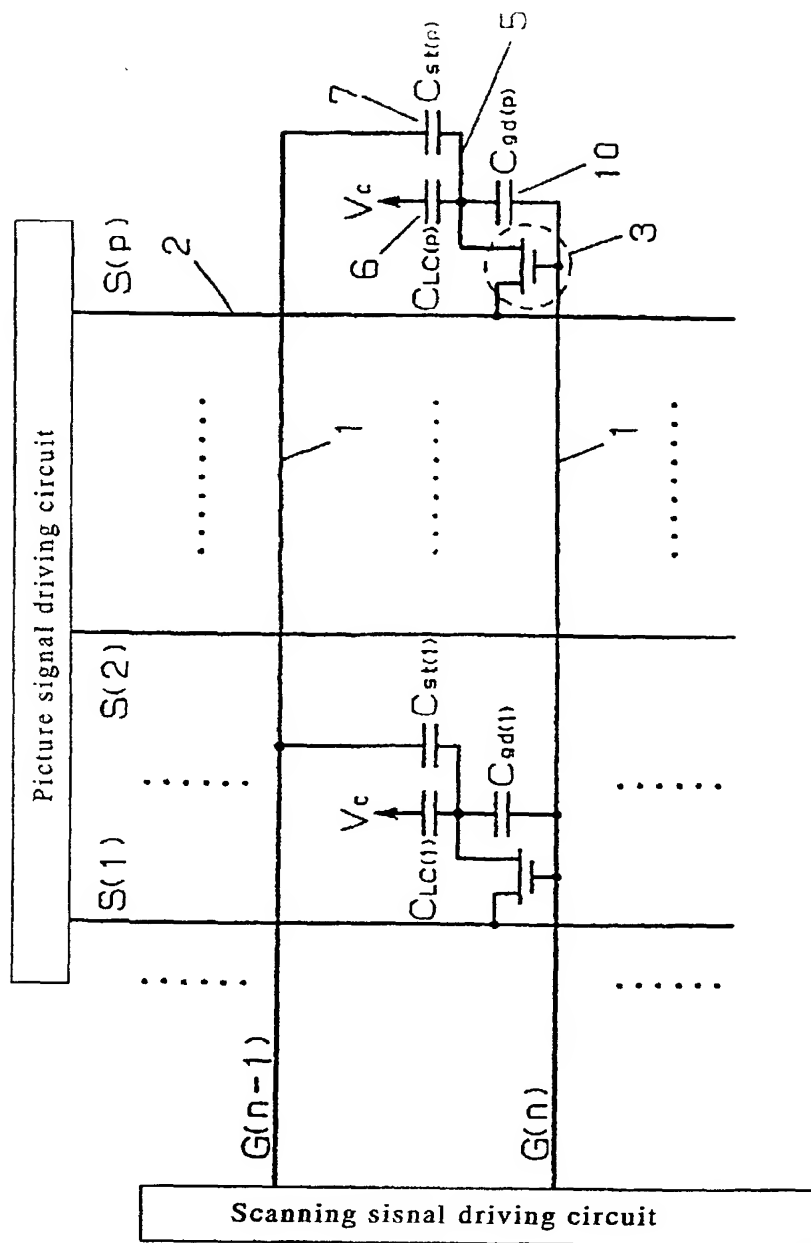


FIG. 1

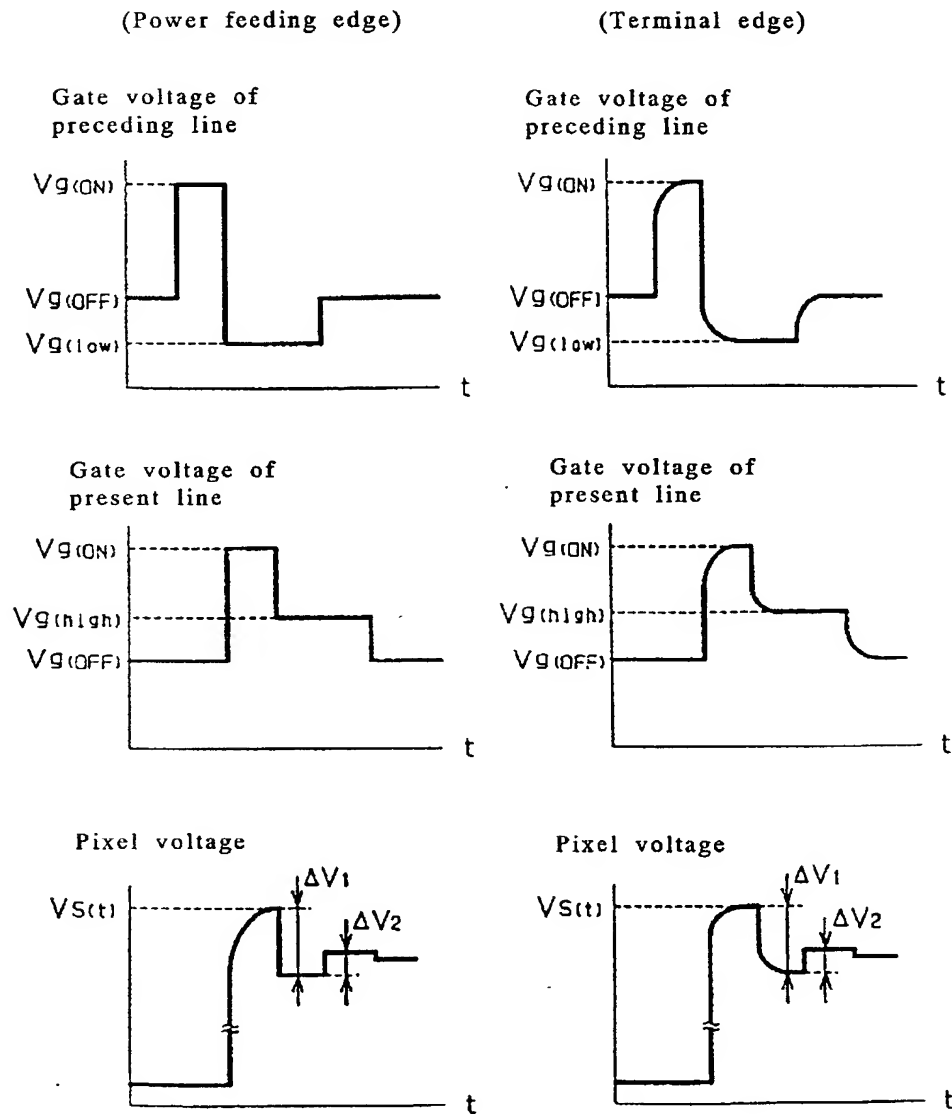


FIG. 2

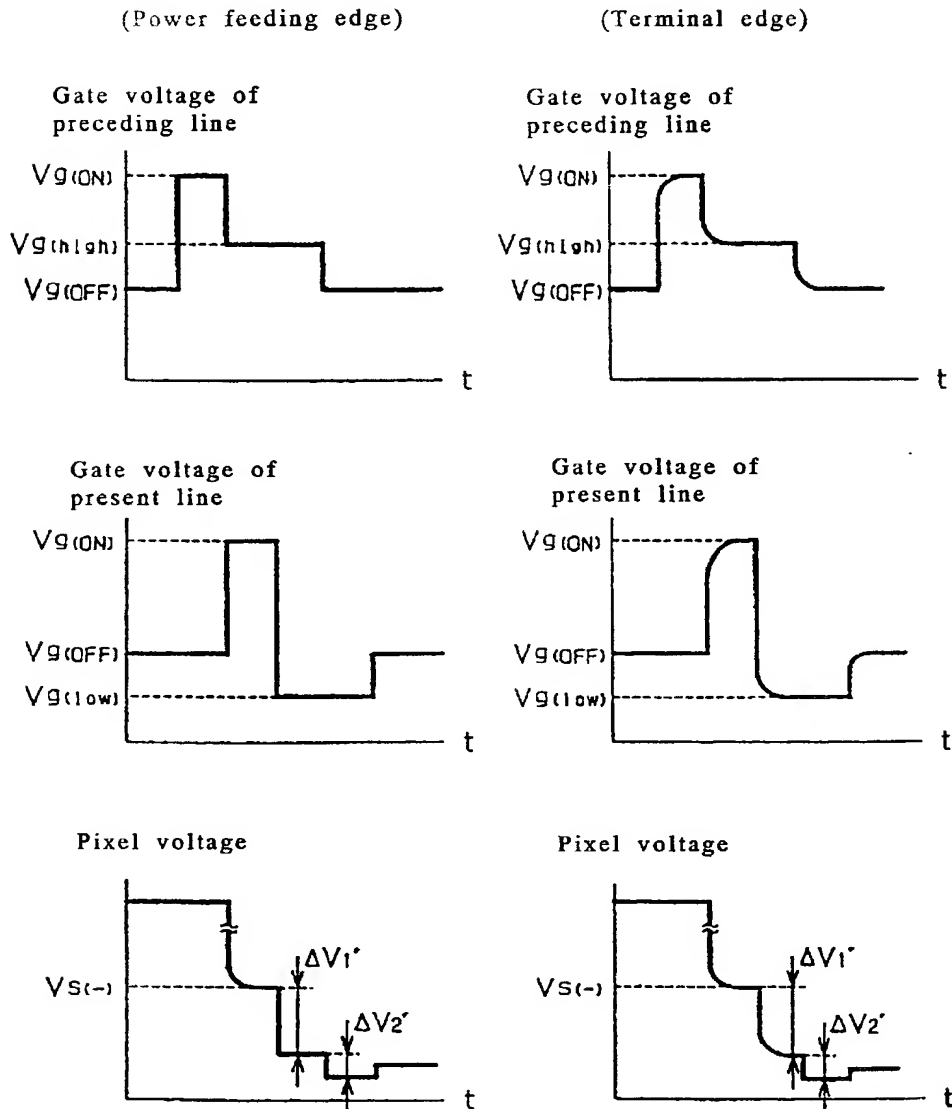


FIG. 3

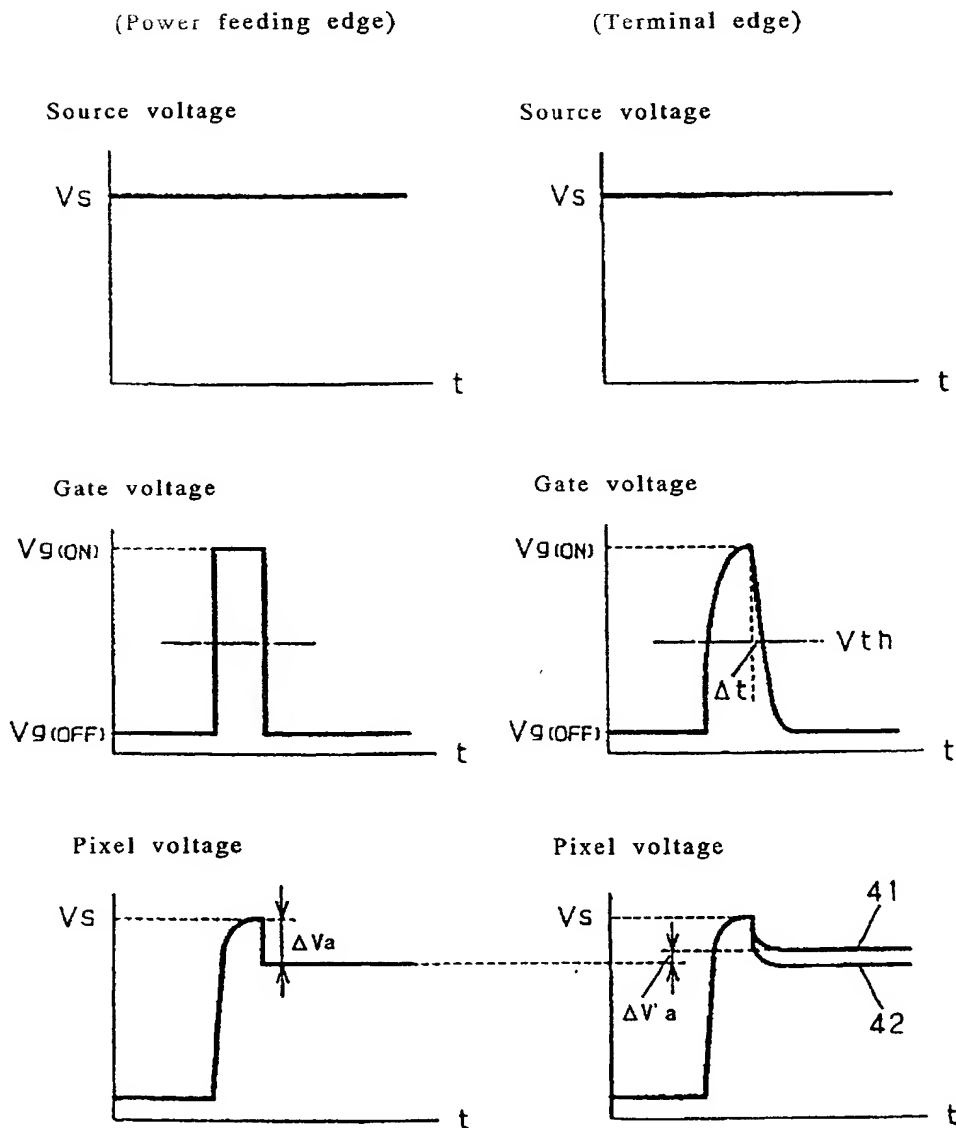


FIG. 4

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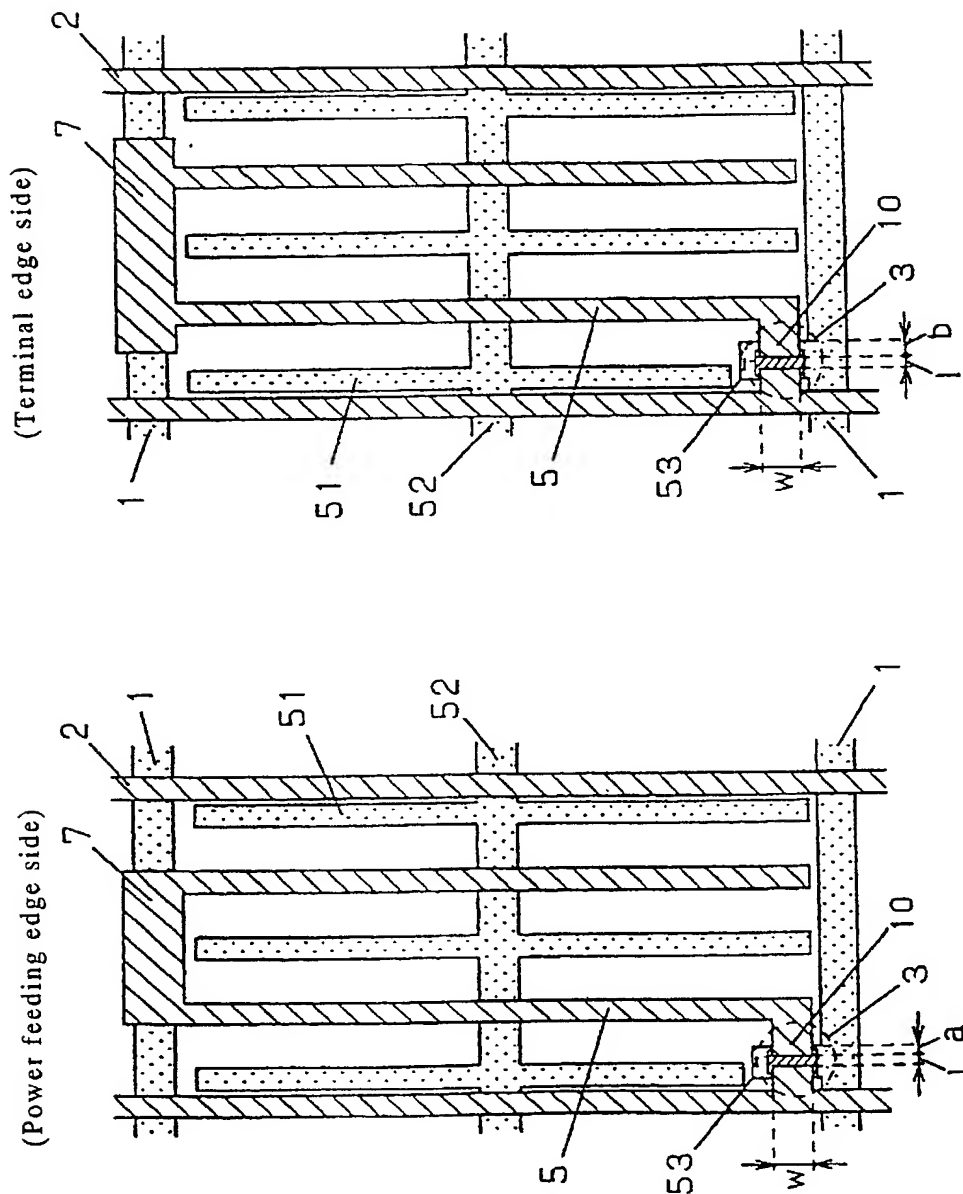


FIG. 5

(Power feeding edge)

(Terminal edge)

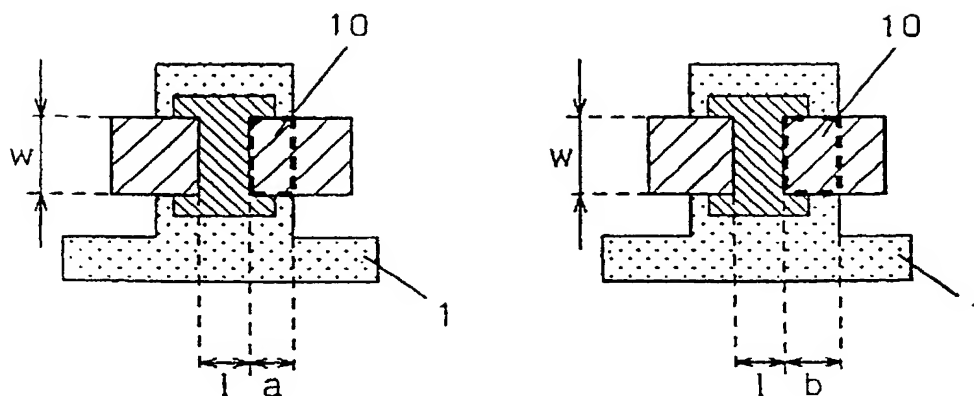


FIG. 6

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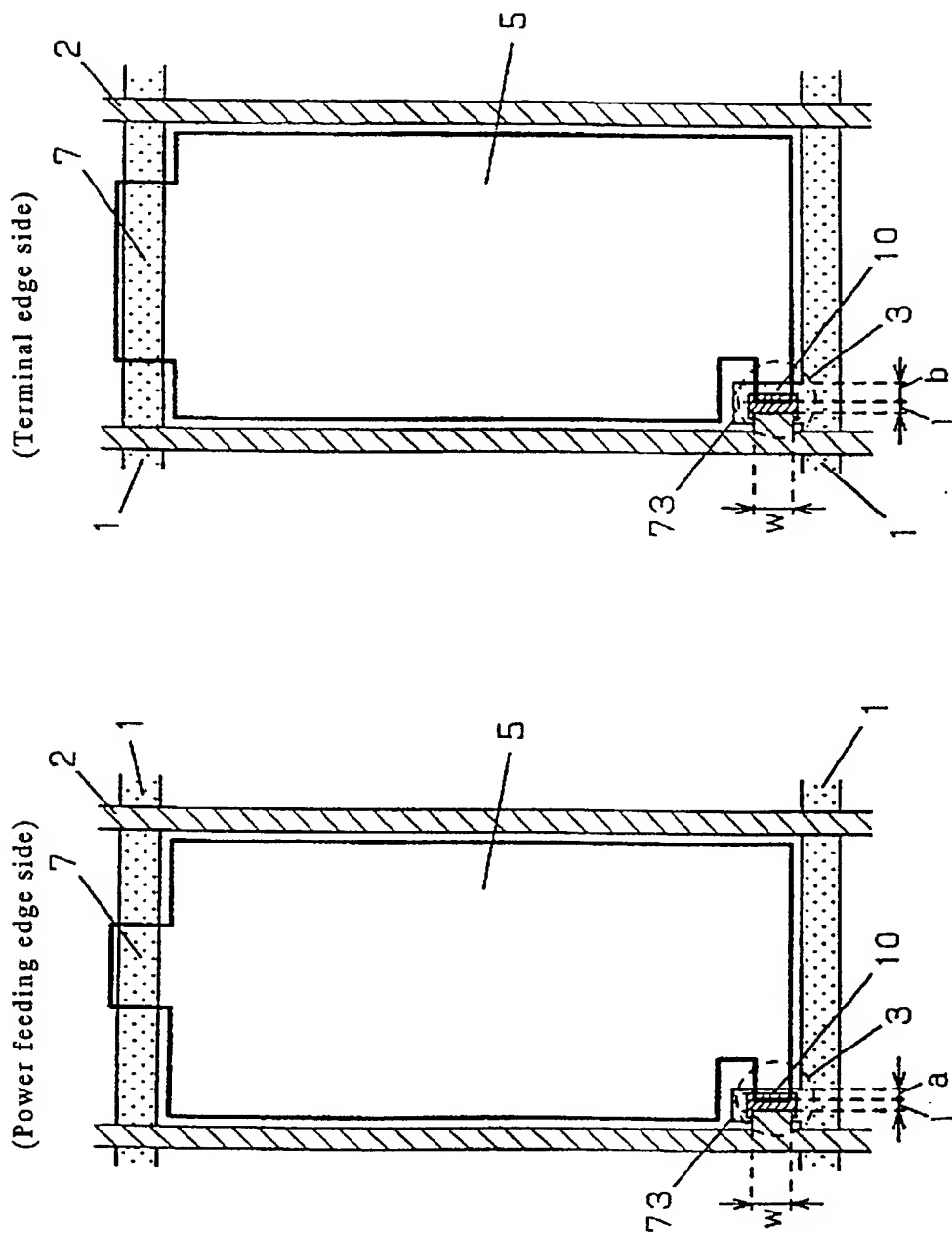


FIG. 7

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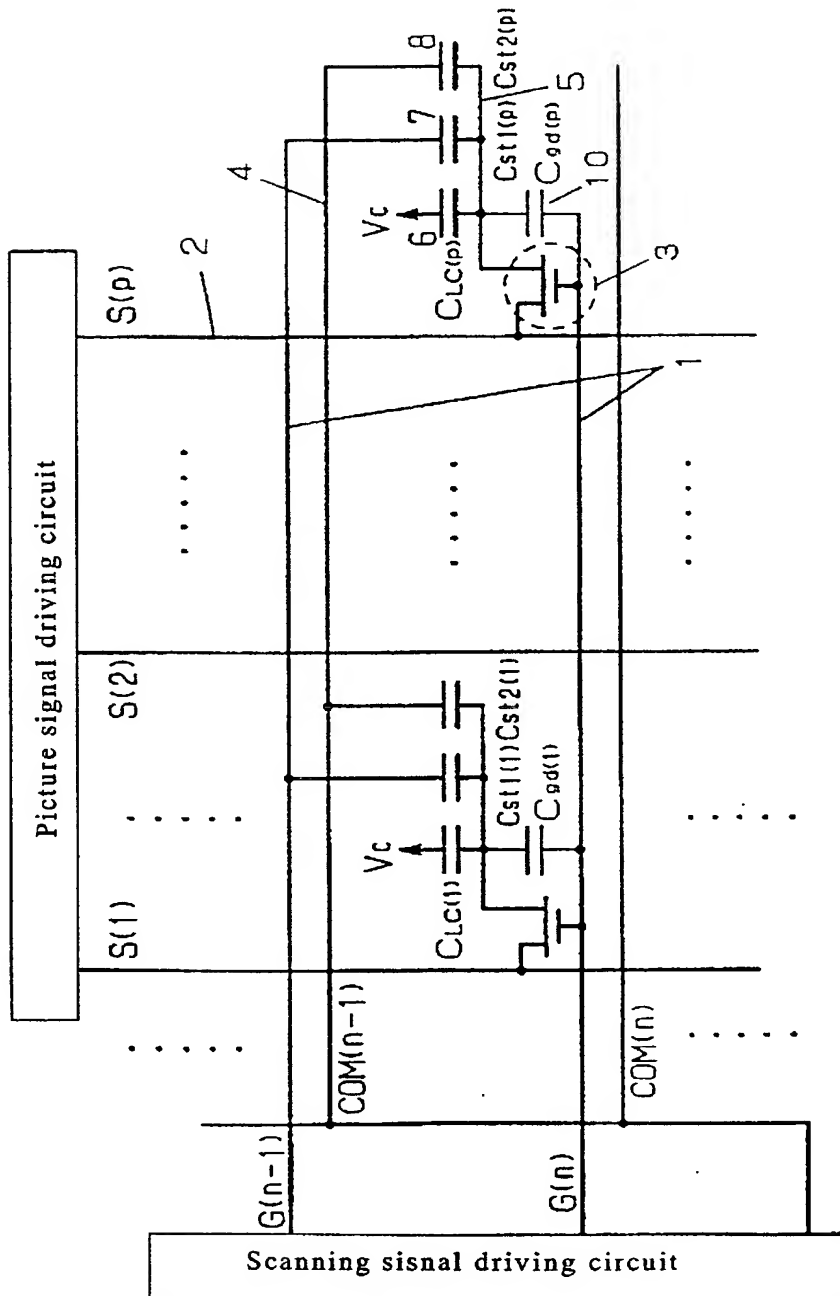


FIG. 8



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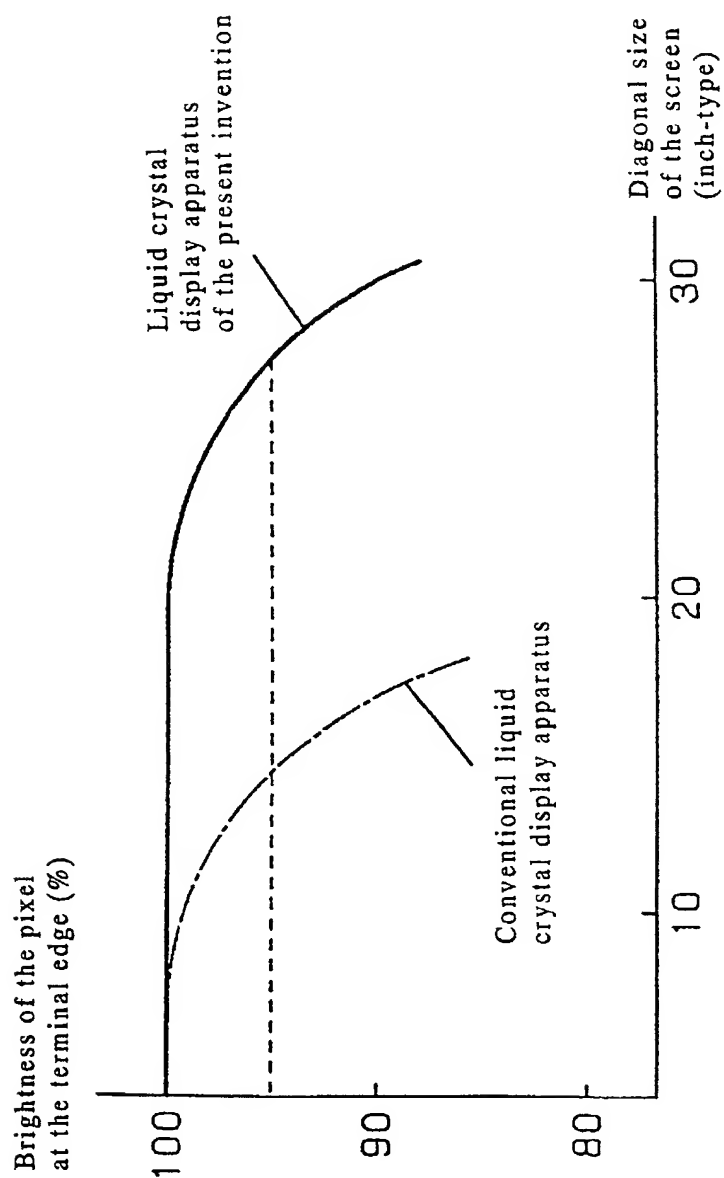


FIG. 9

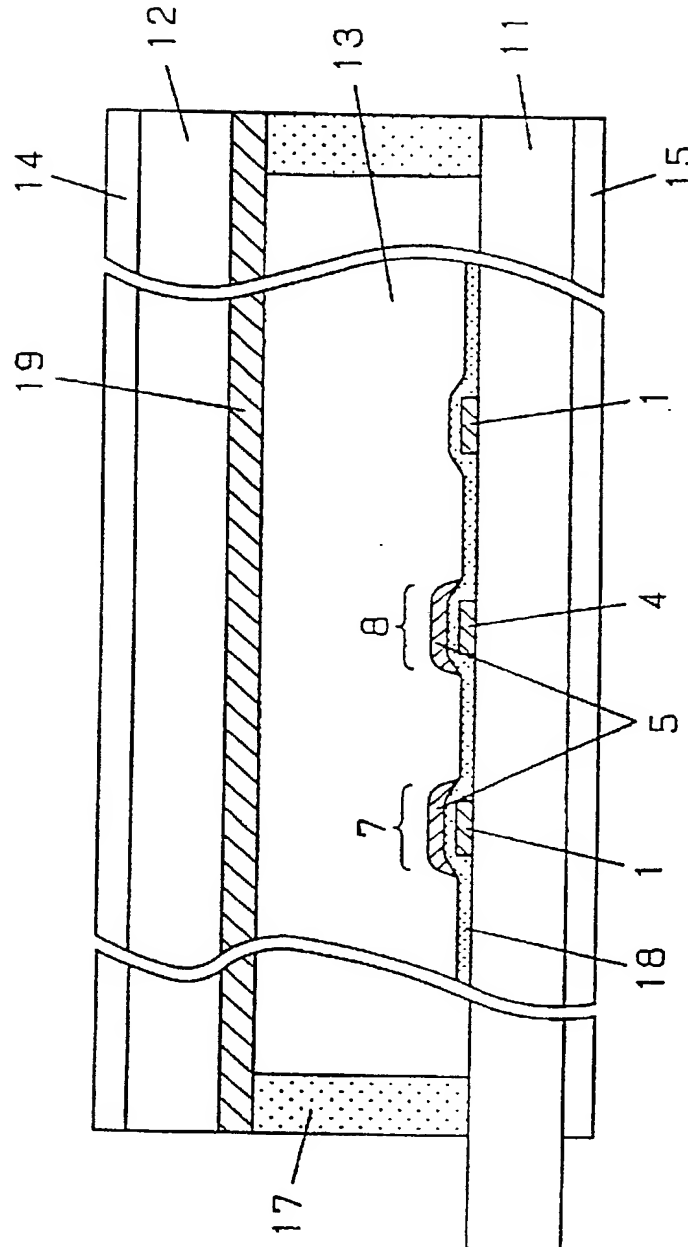


FIG. 10

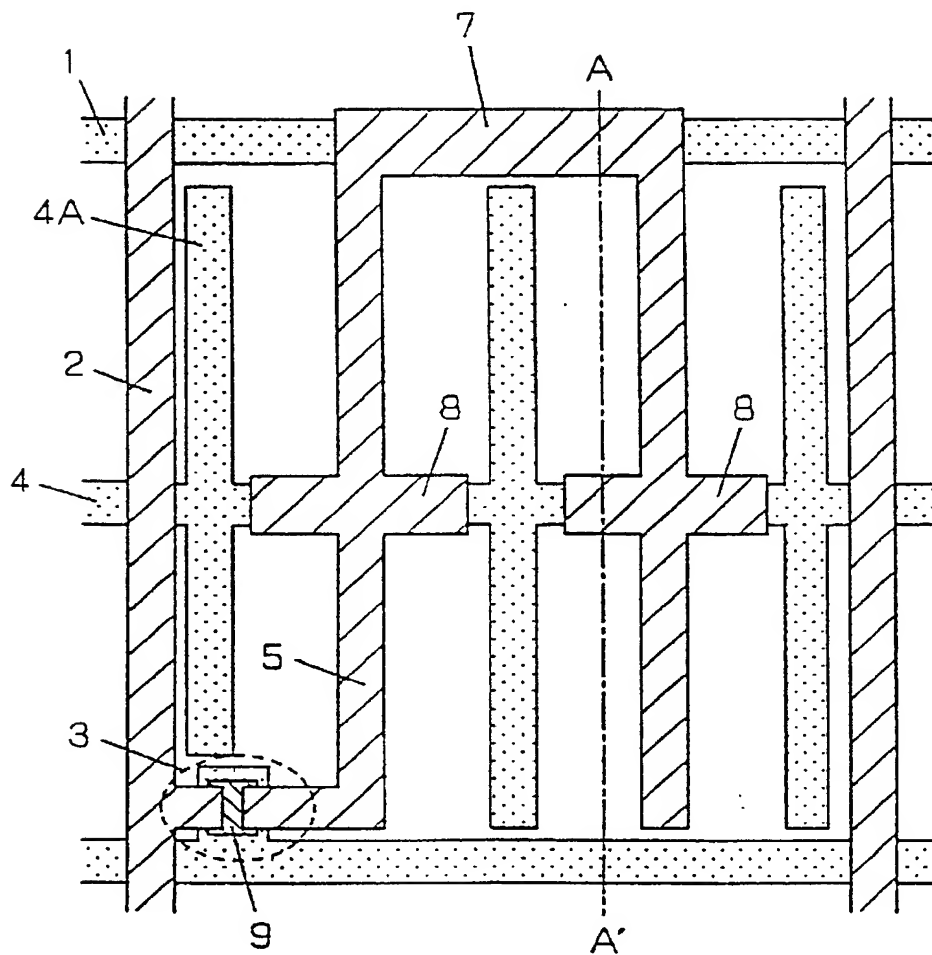


FIG. 11

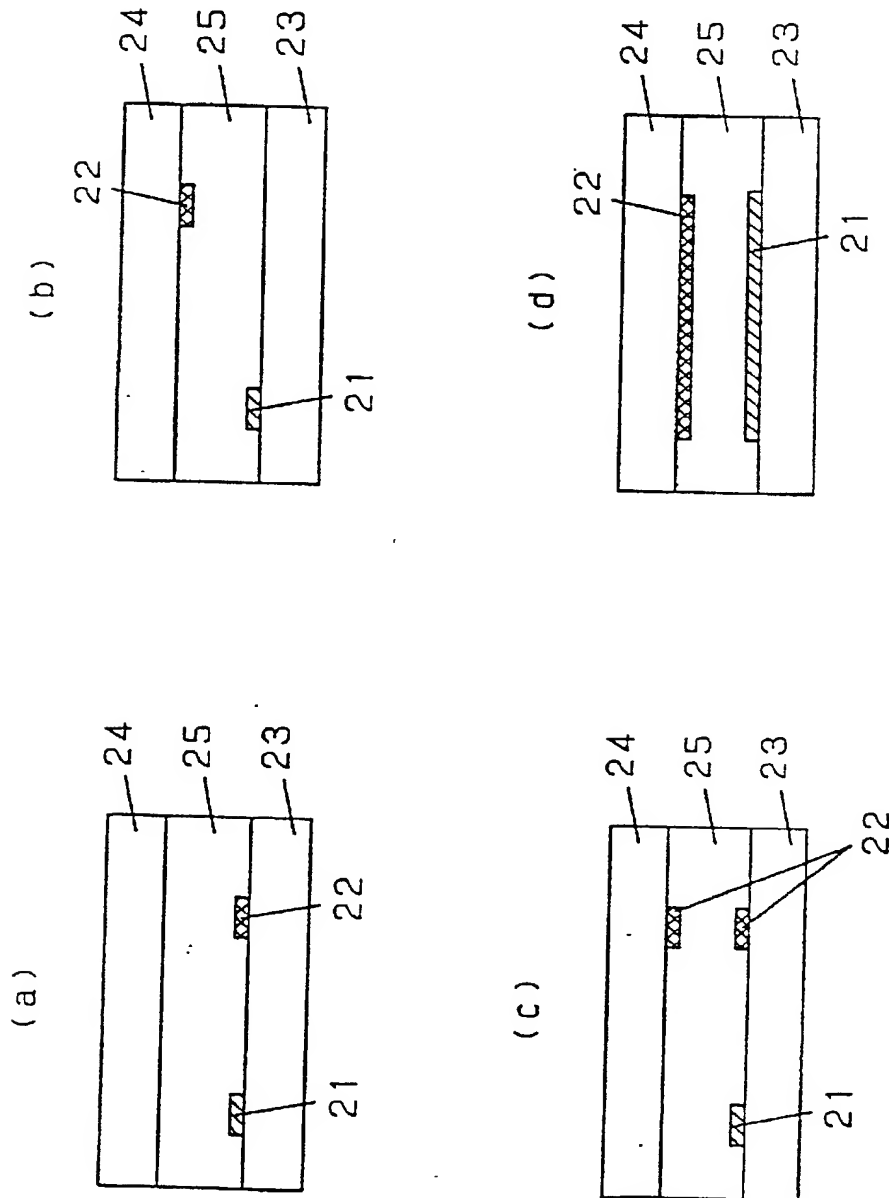


FIG. 12

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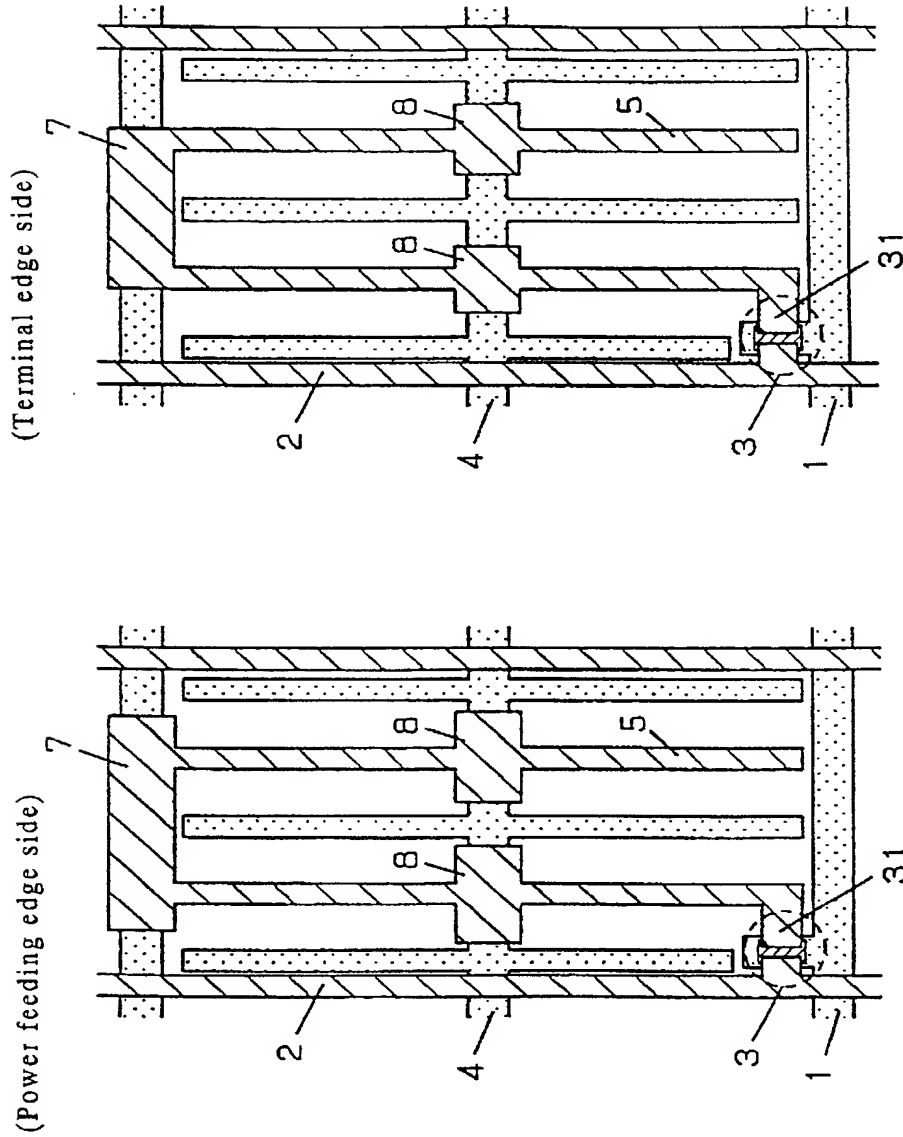


FIG. 13

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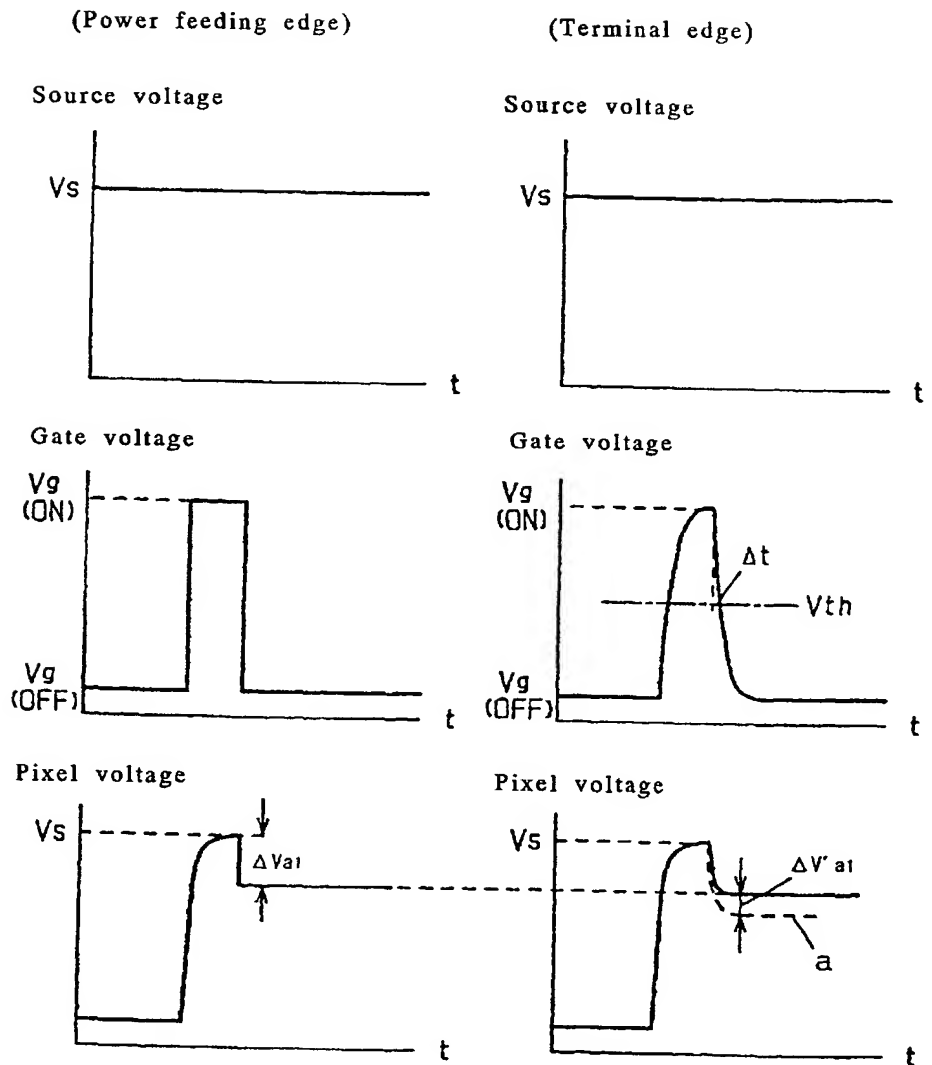


FIG. 14

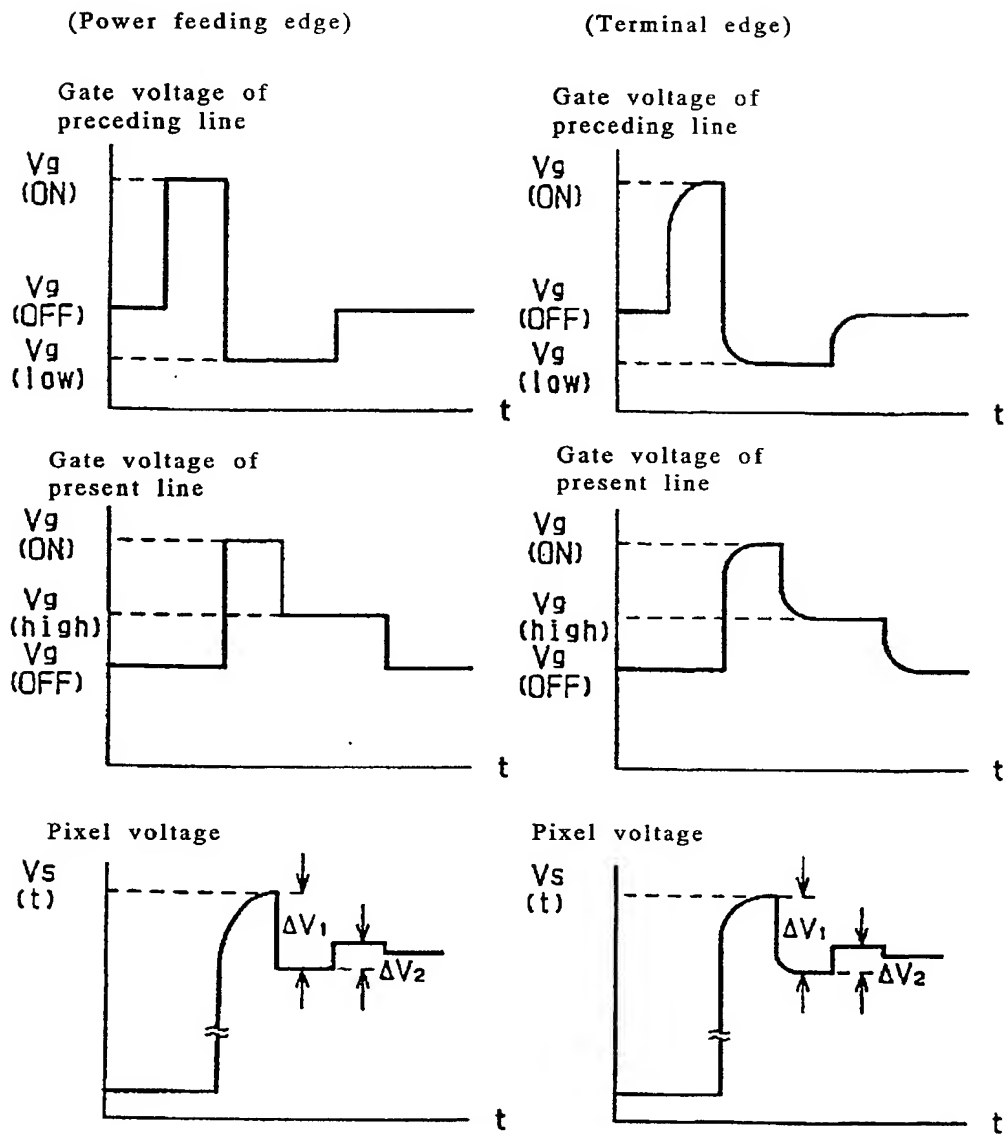


FIG. 15

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(Power feeding edge)

(Terminal edge)

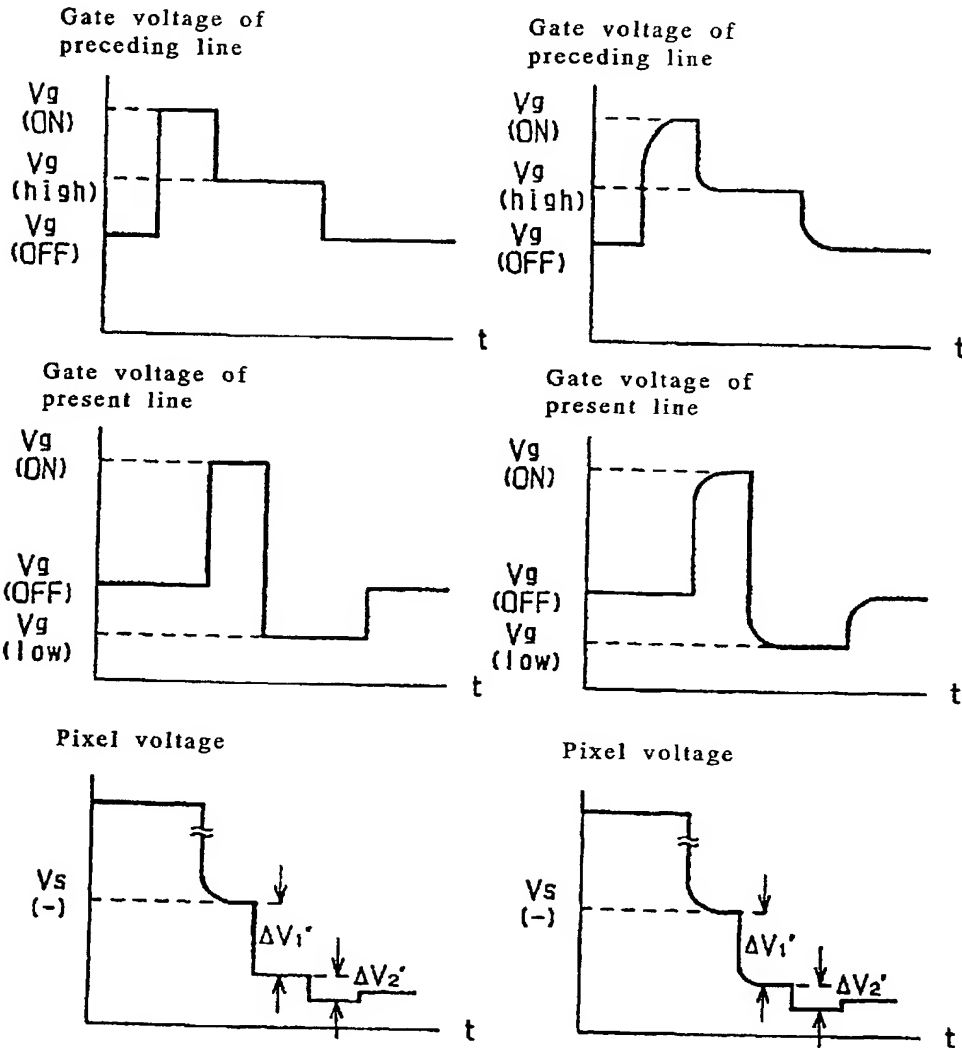


FIG. 16



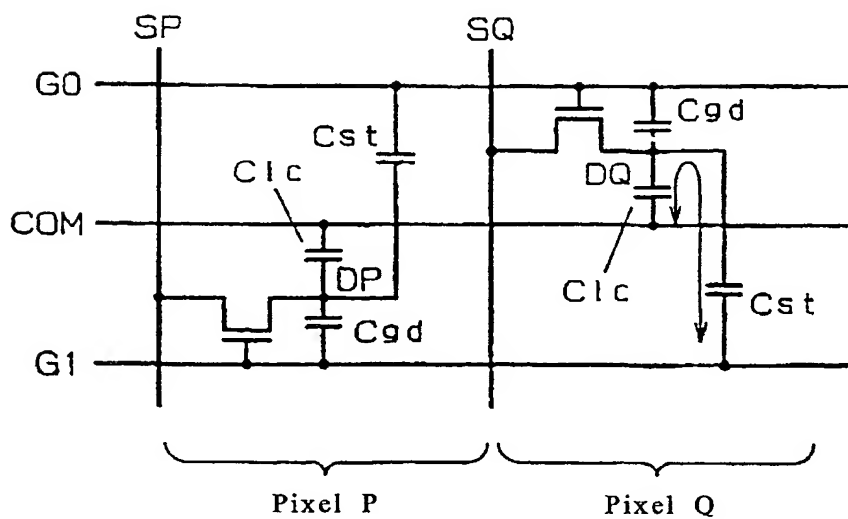


FIG. 17

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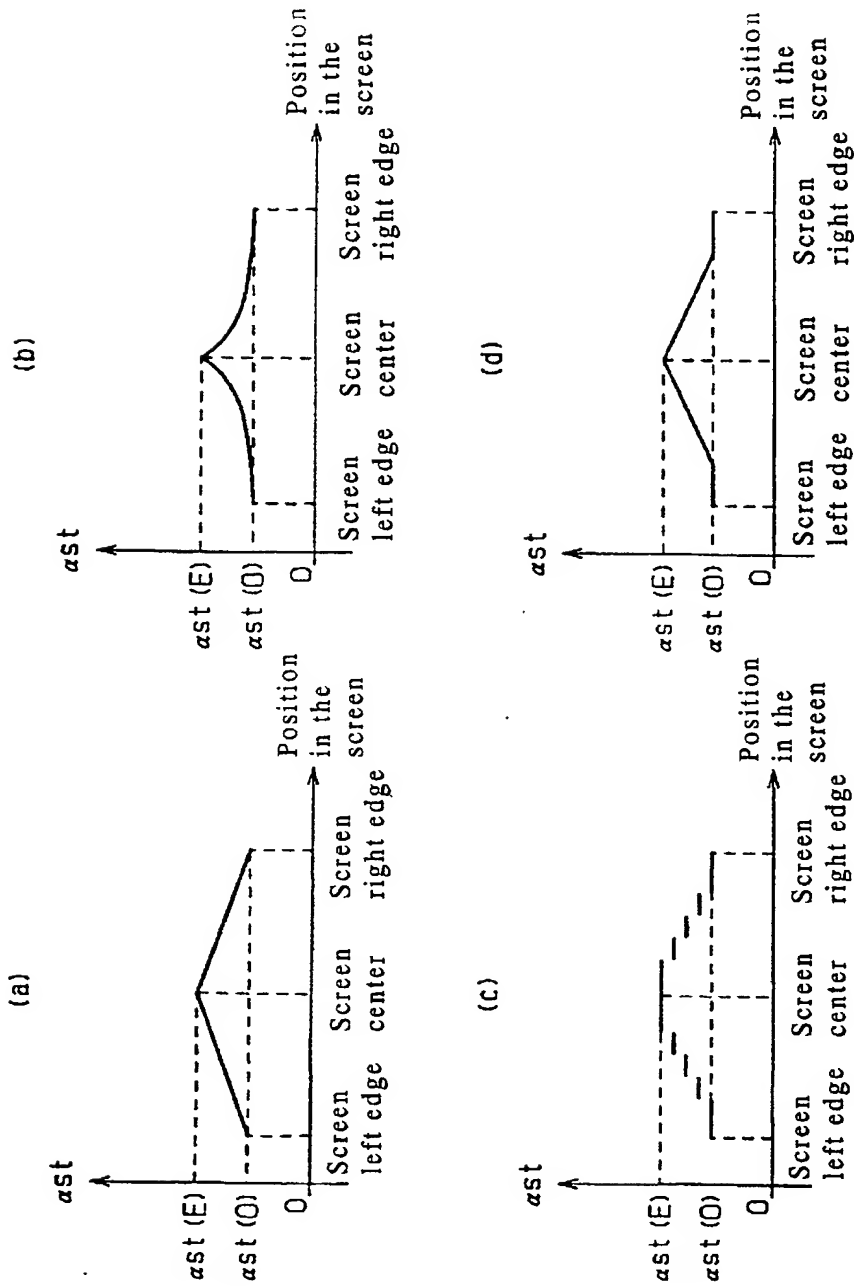
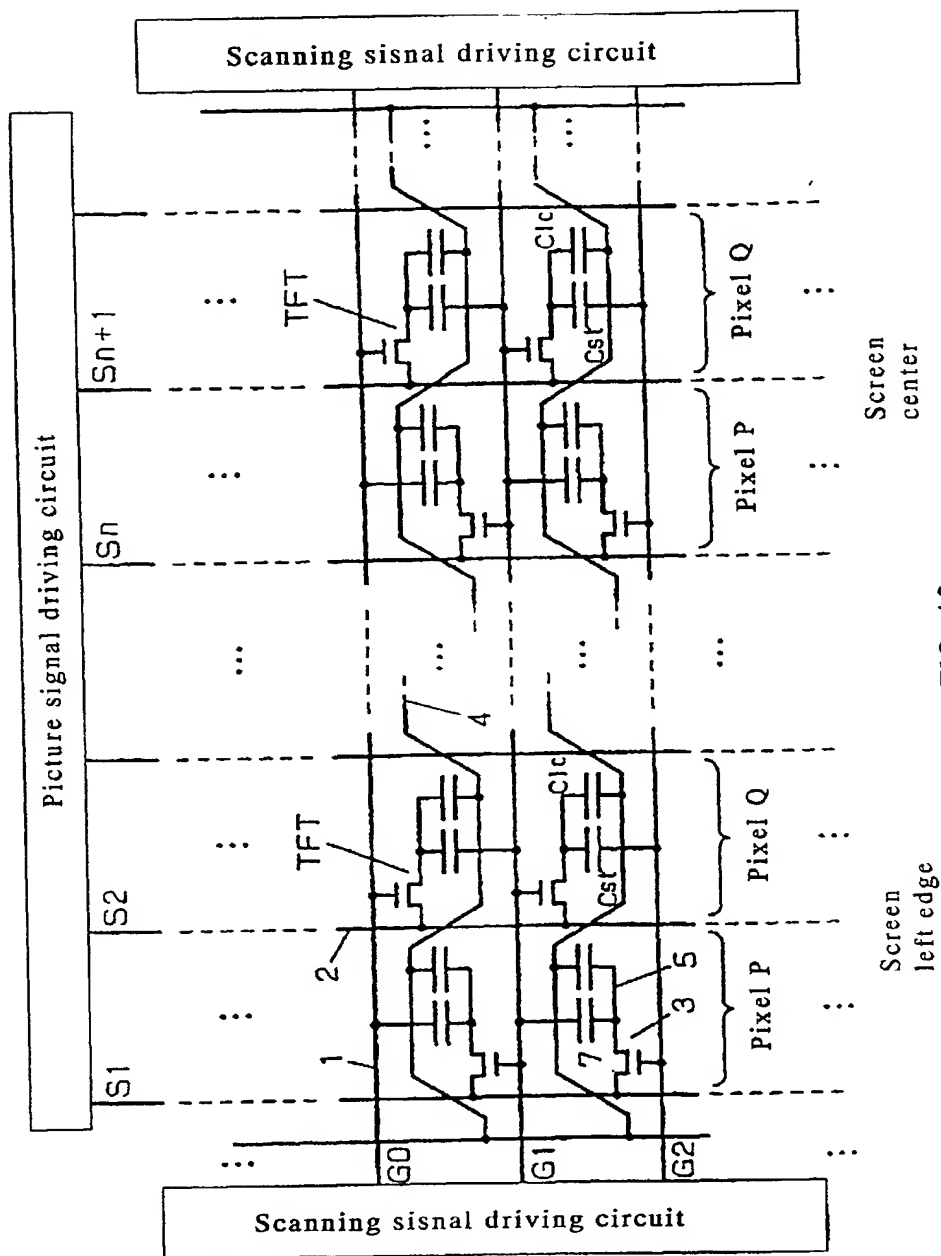
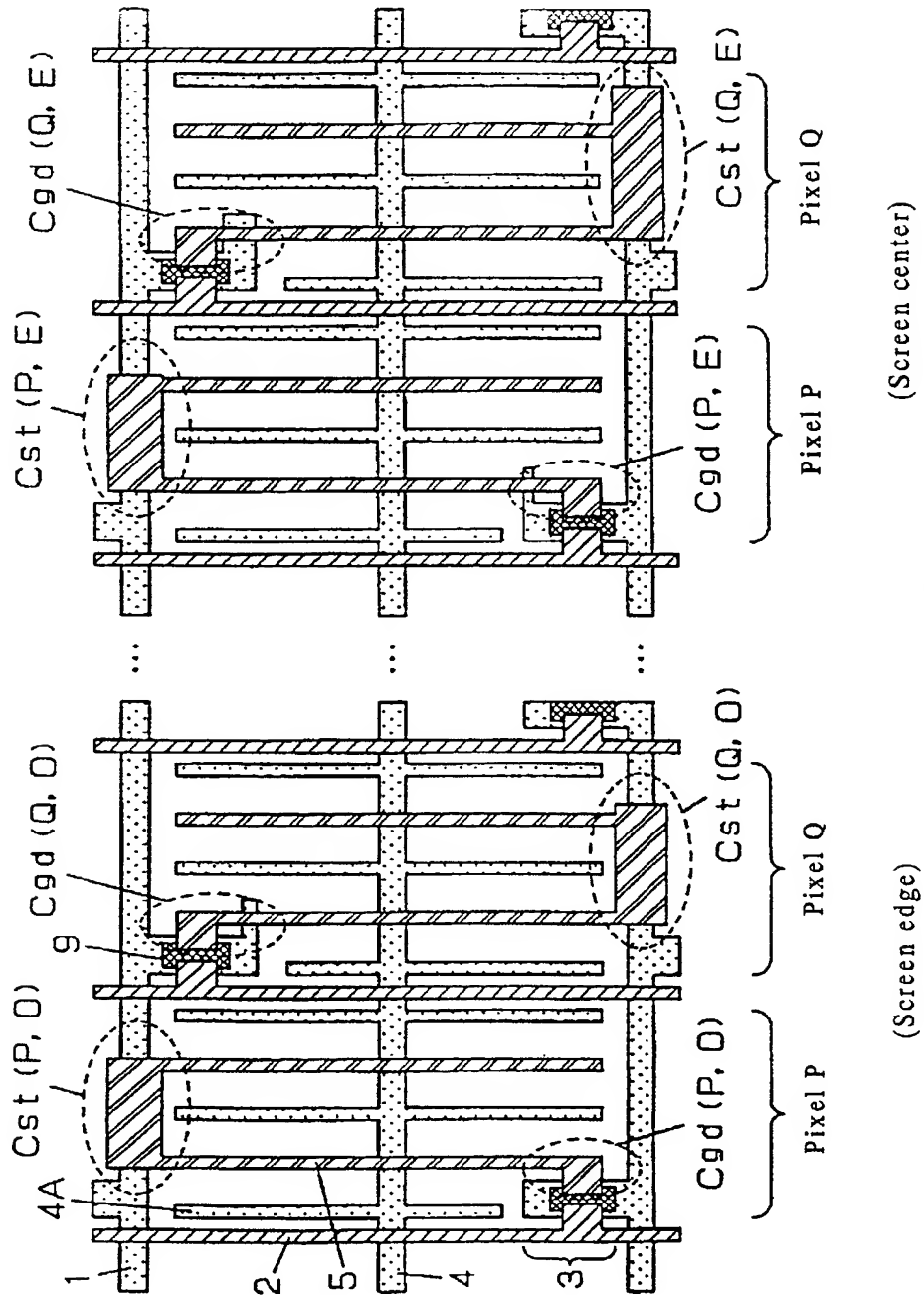


FIG. 18

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FIG. 21

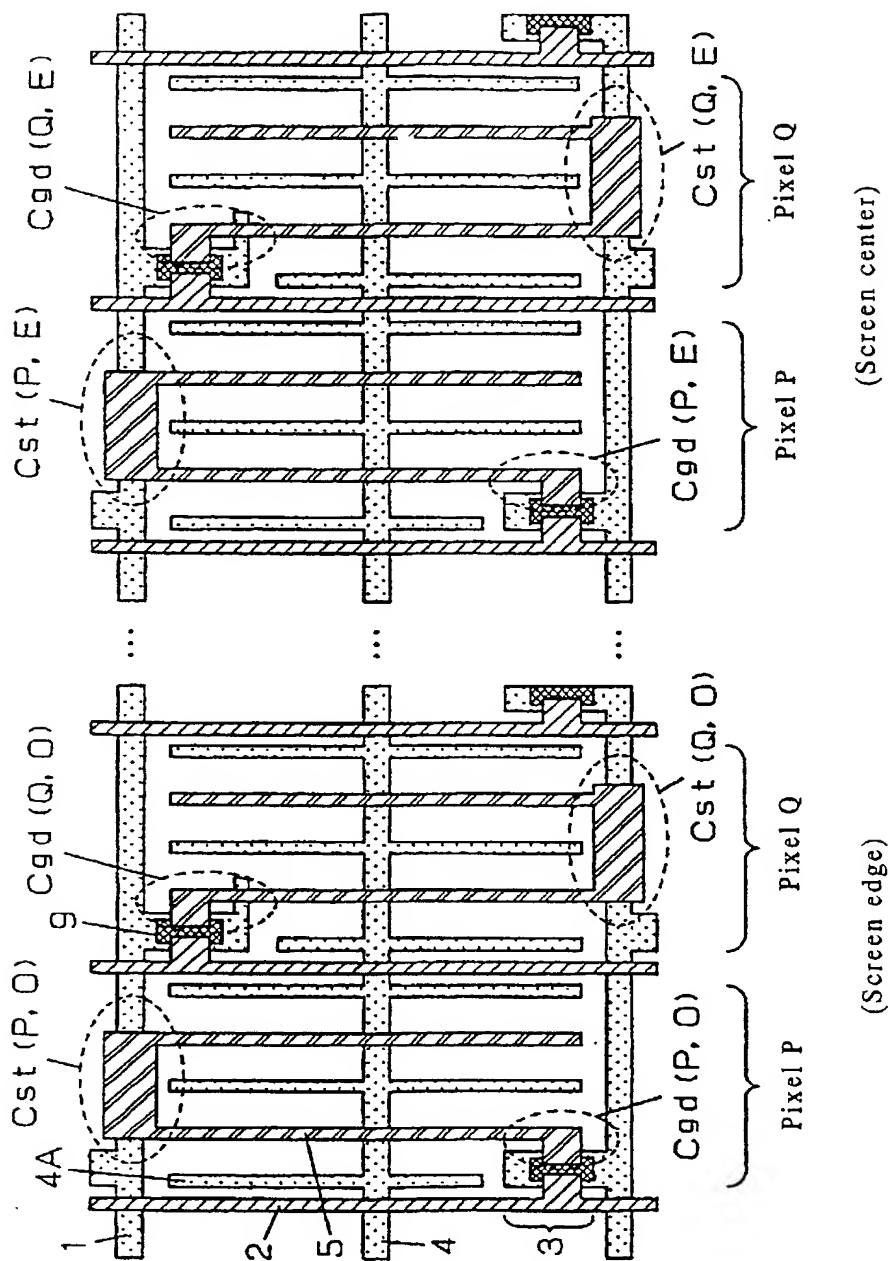


FIG. 21

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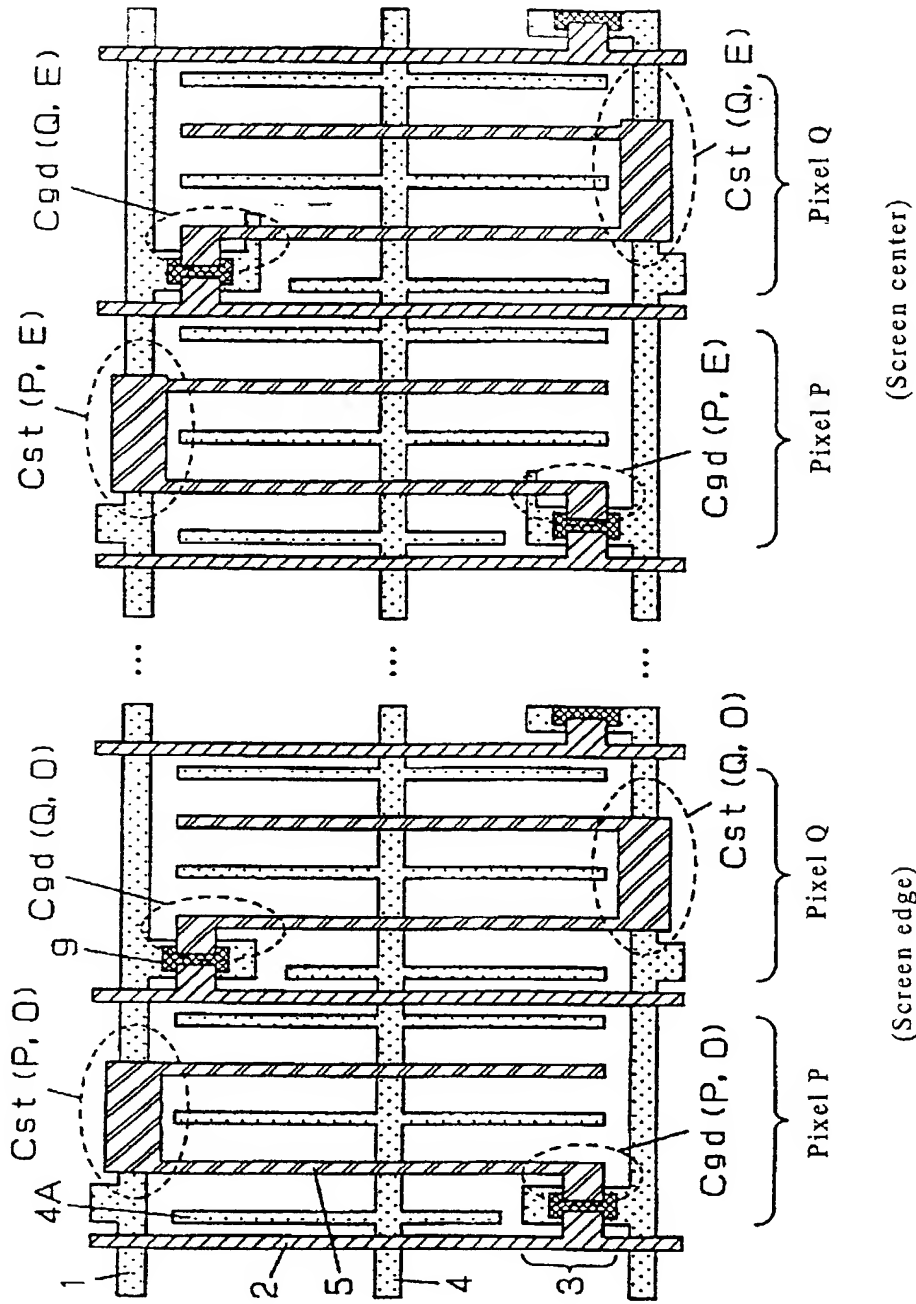


FIG. 22

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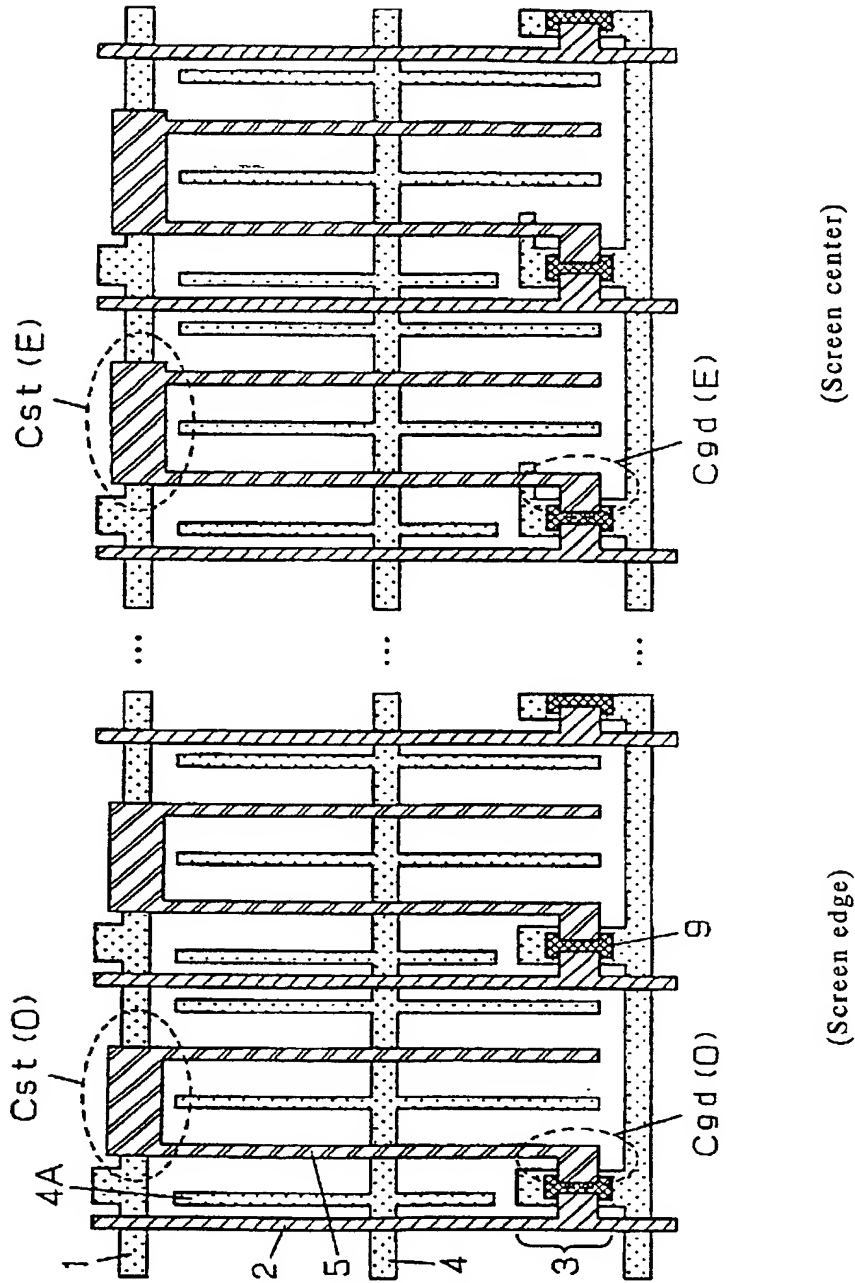


FIG. 23

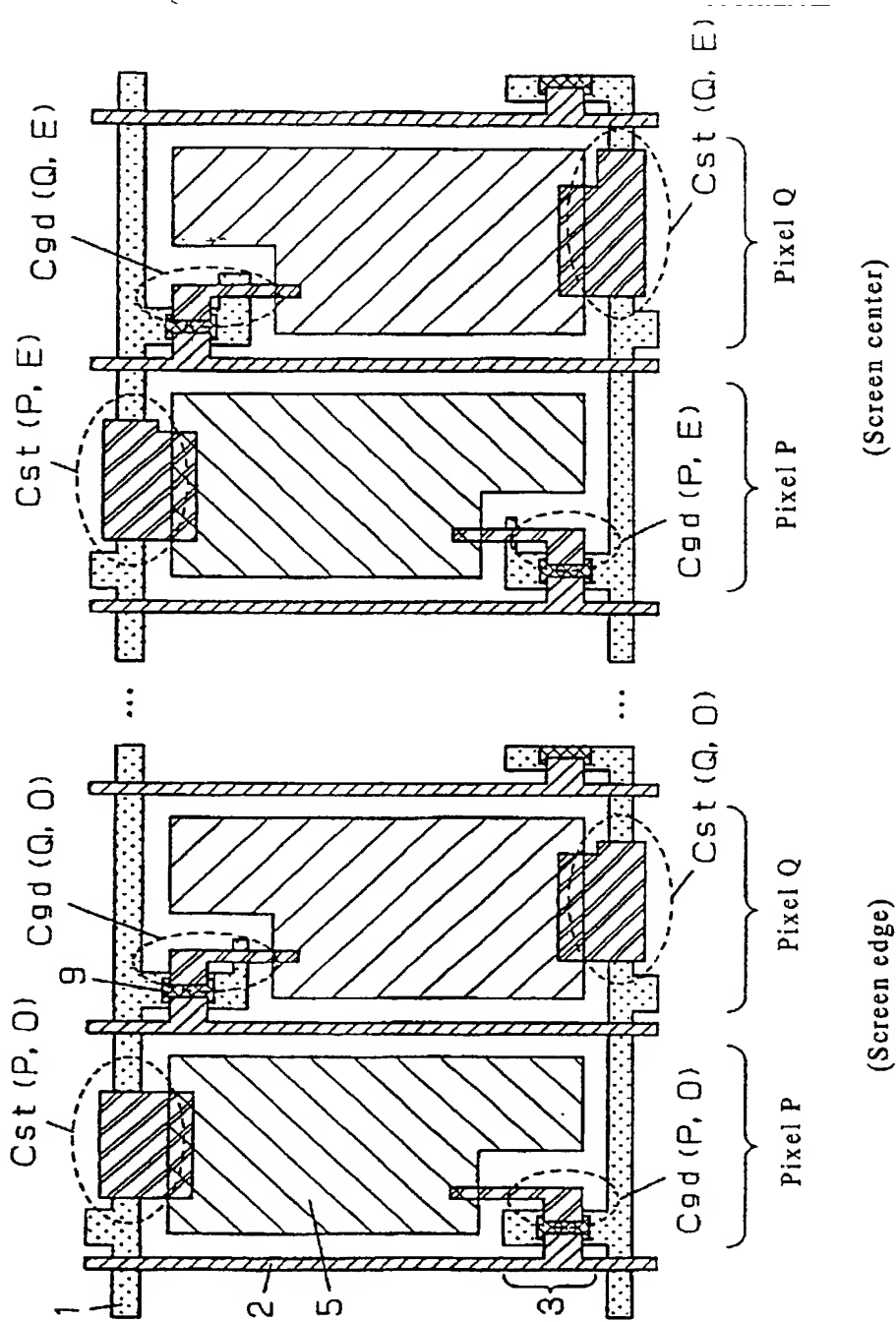
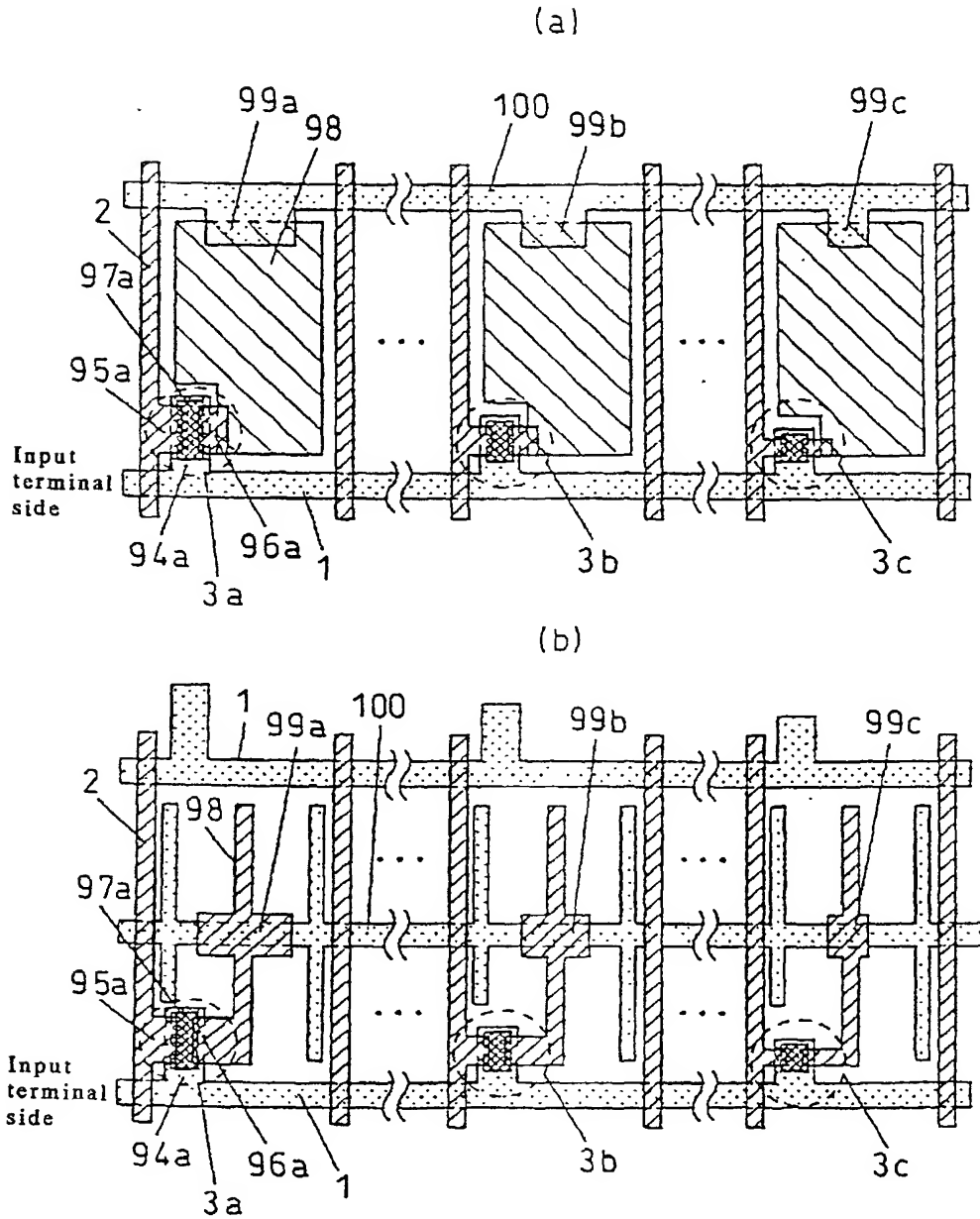


FIG. 24





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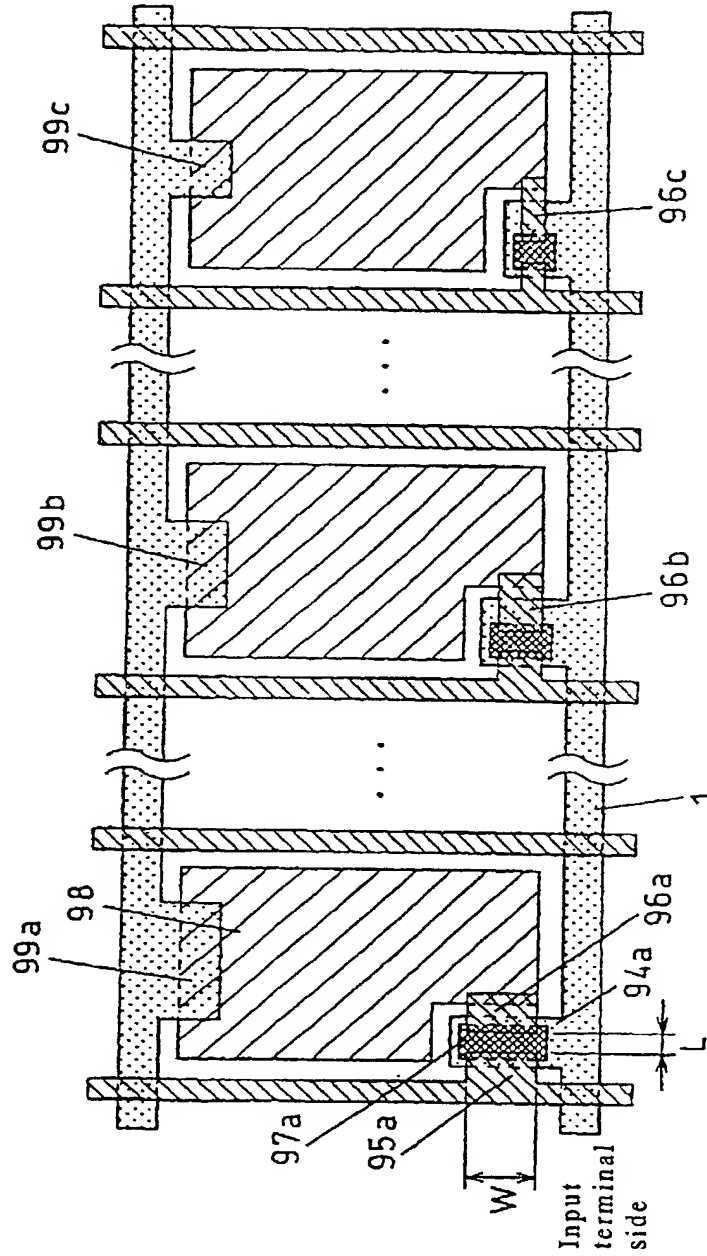


FIG. 26

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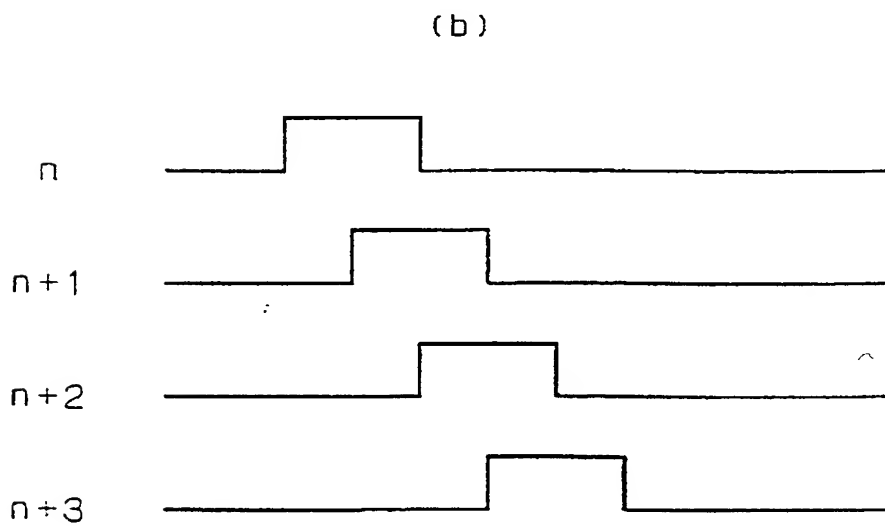
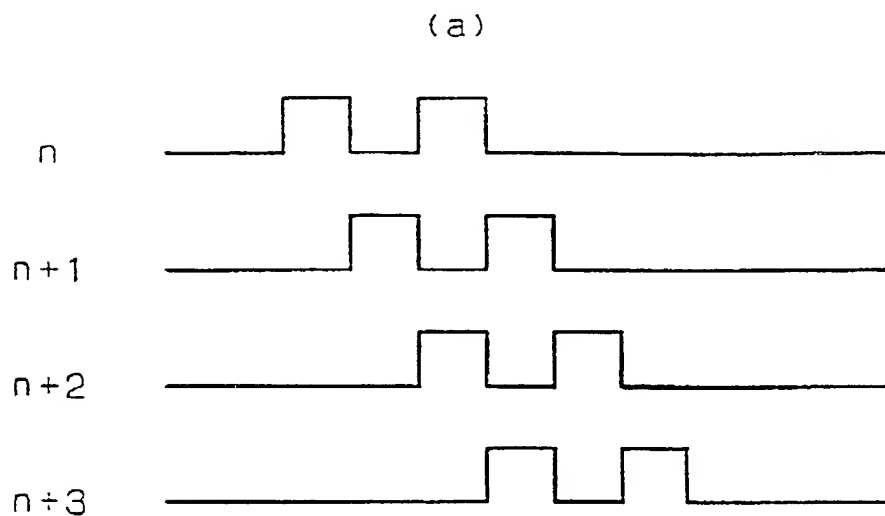


FIG. 27

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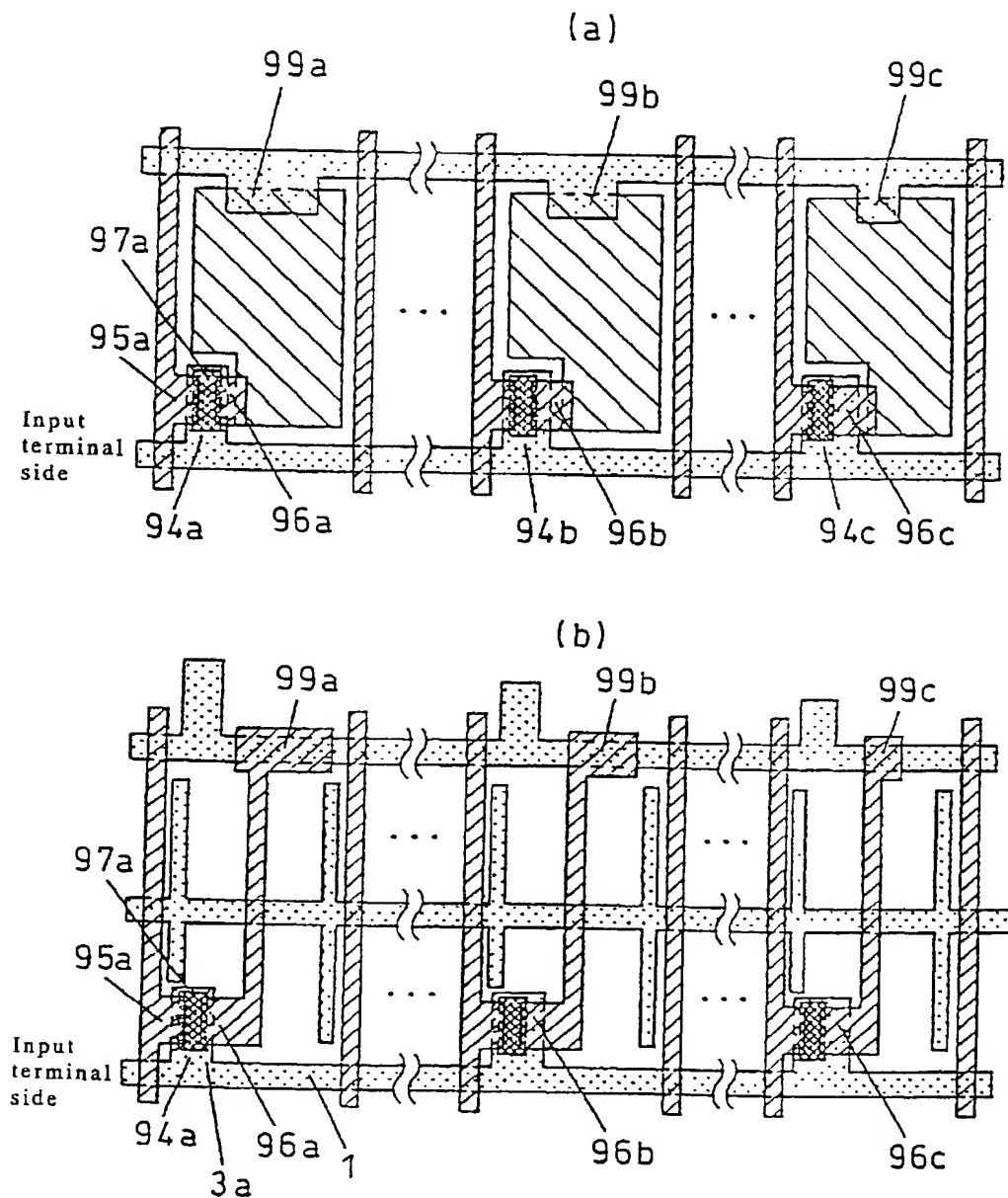
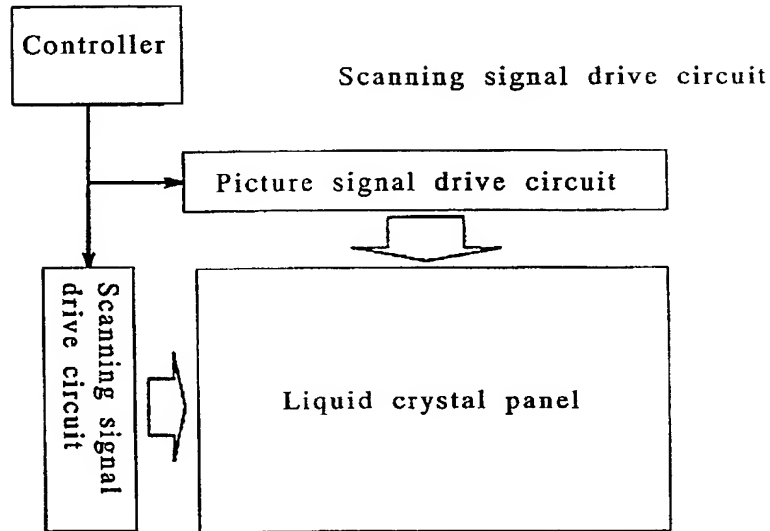


FIG. 28

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(a)



(b)

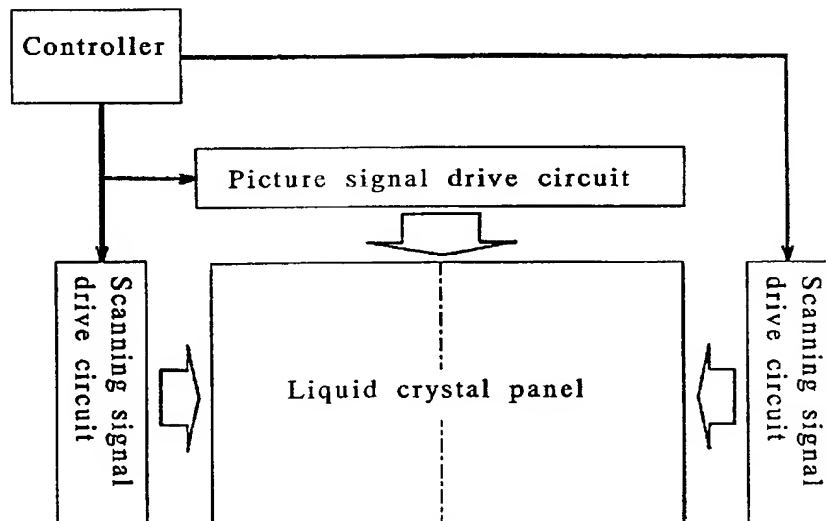


FIG. 29

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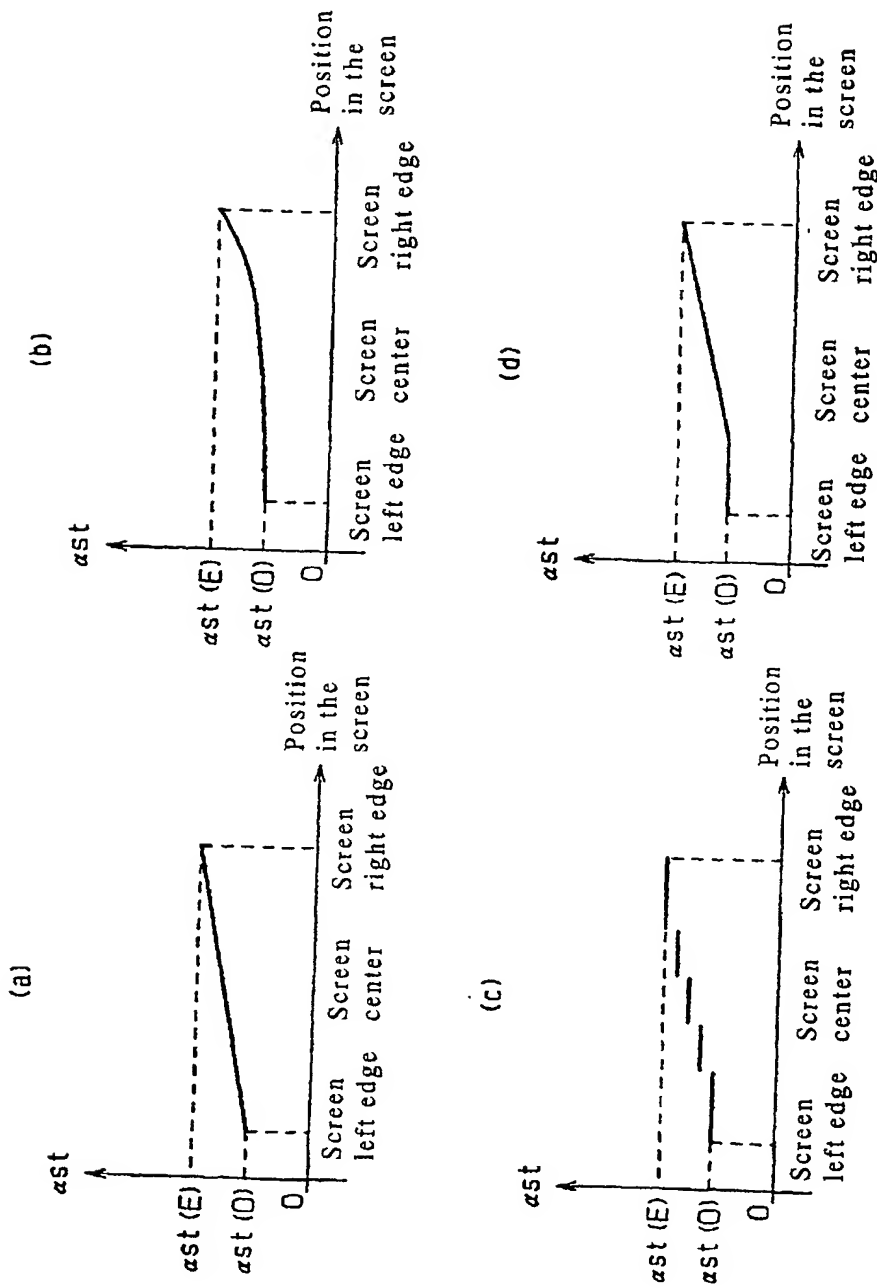


FIG. 30

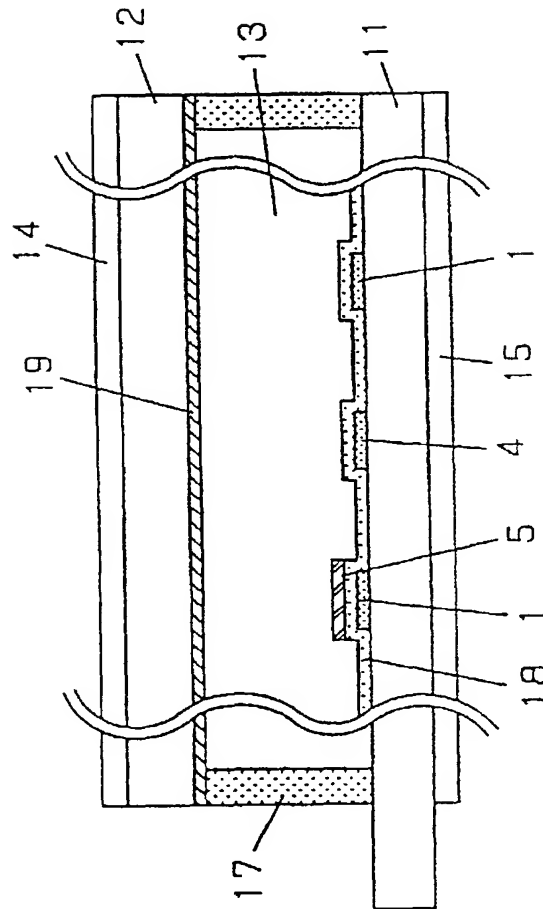


FIG. 31

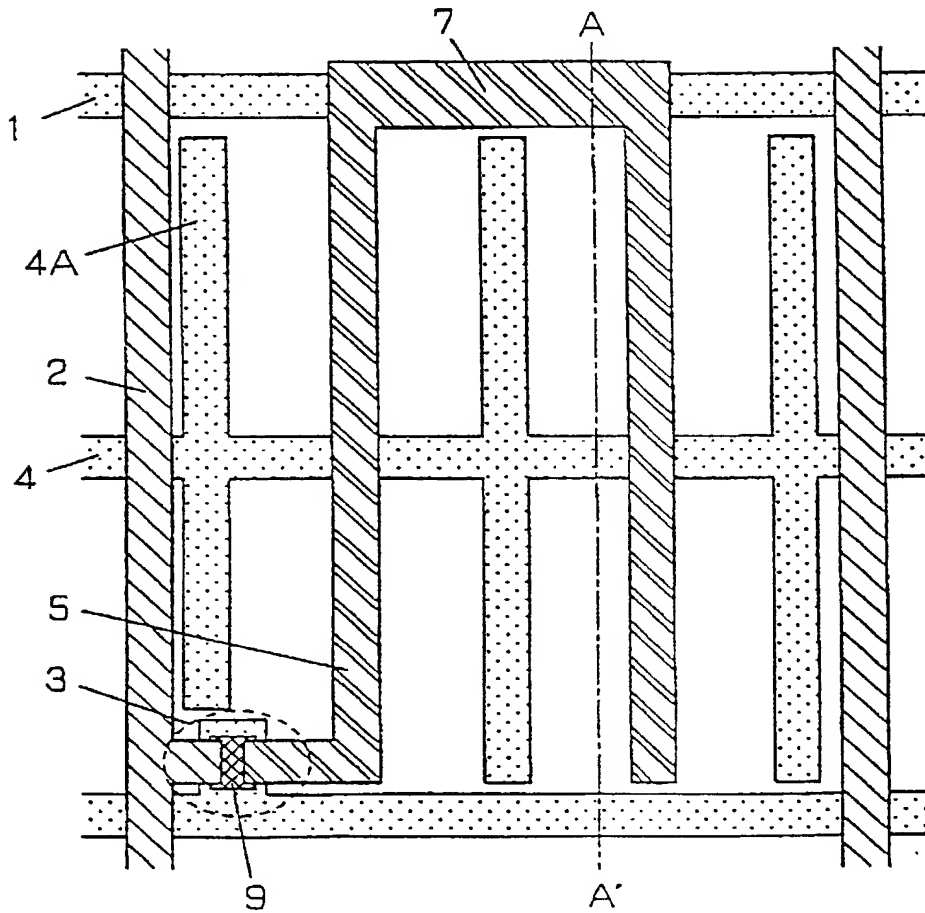


FIG. 32



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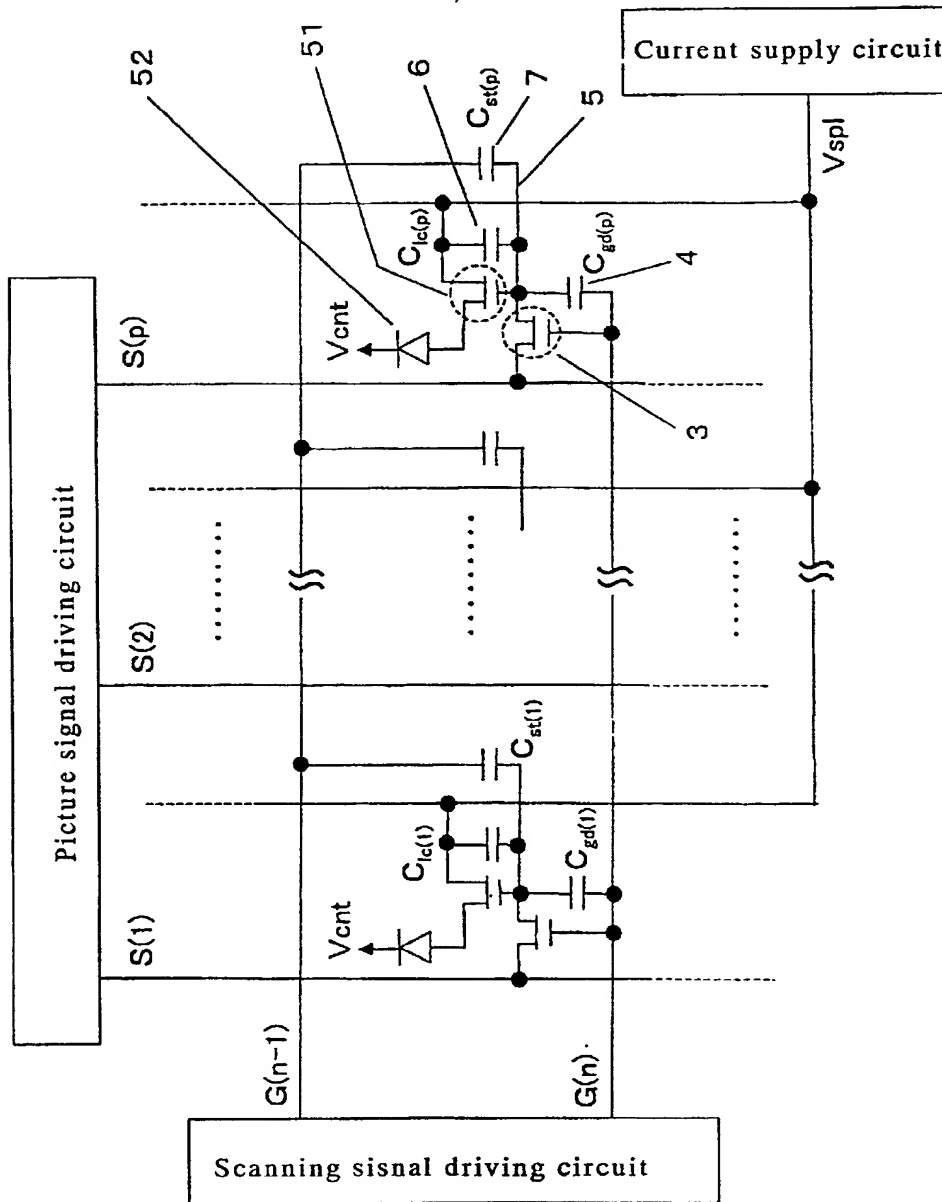


FIG. 33

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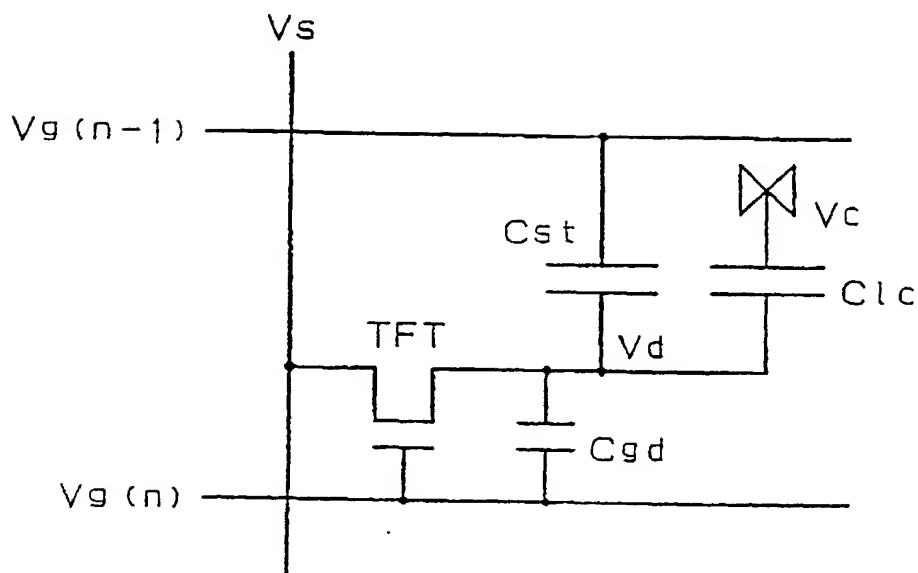
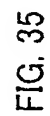


FIG. 34



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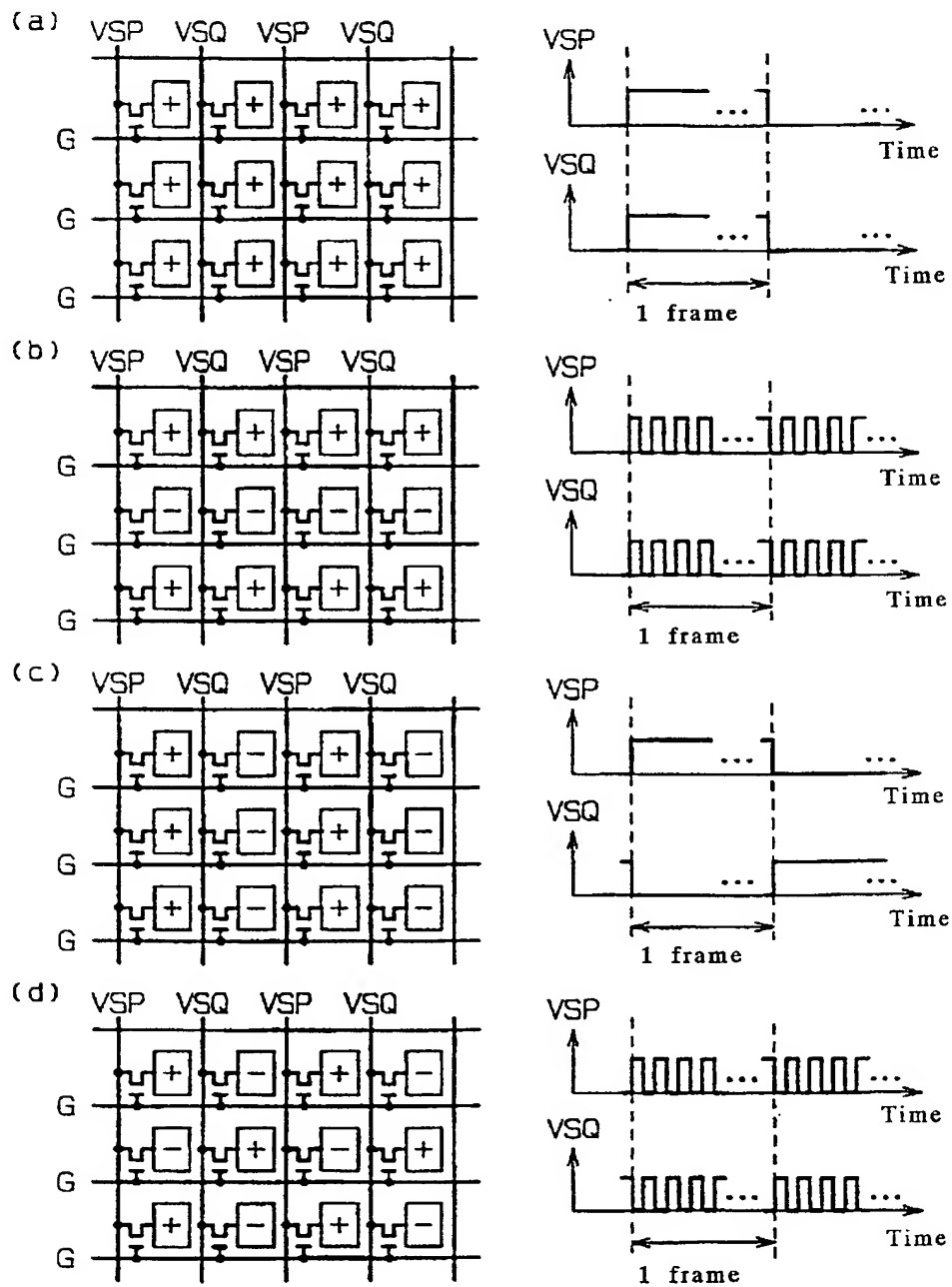


FIG. 36

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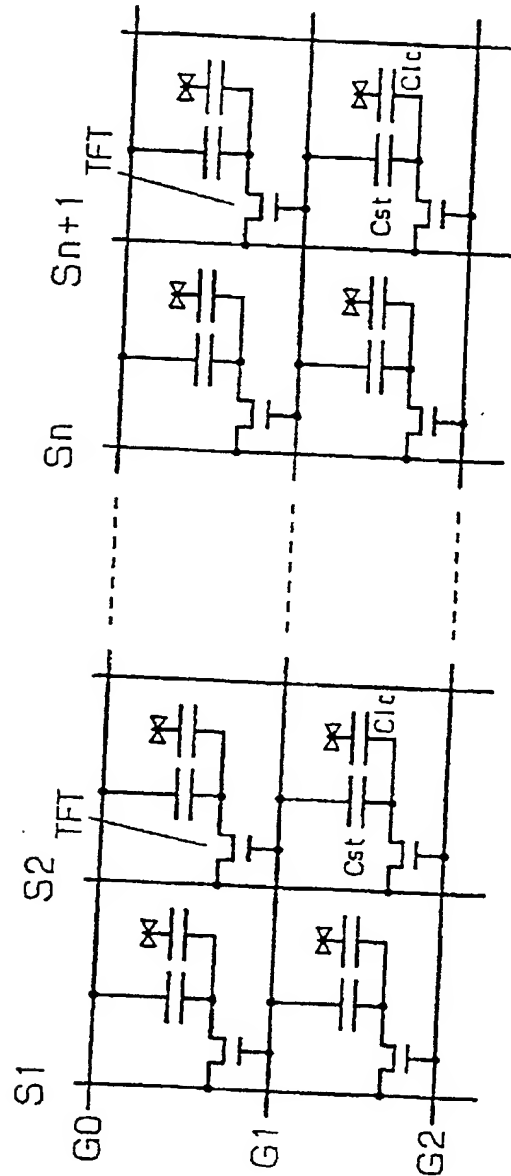


FIG. 37

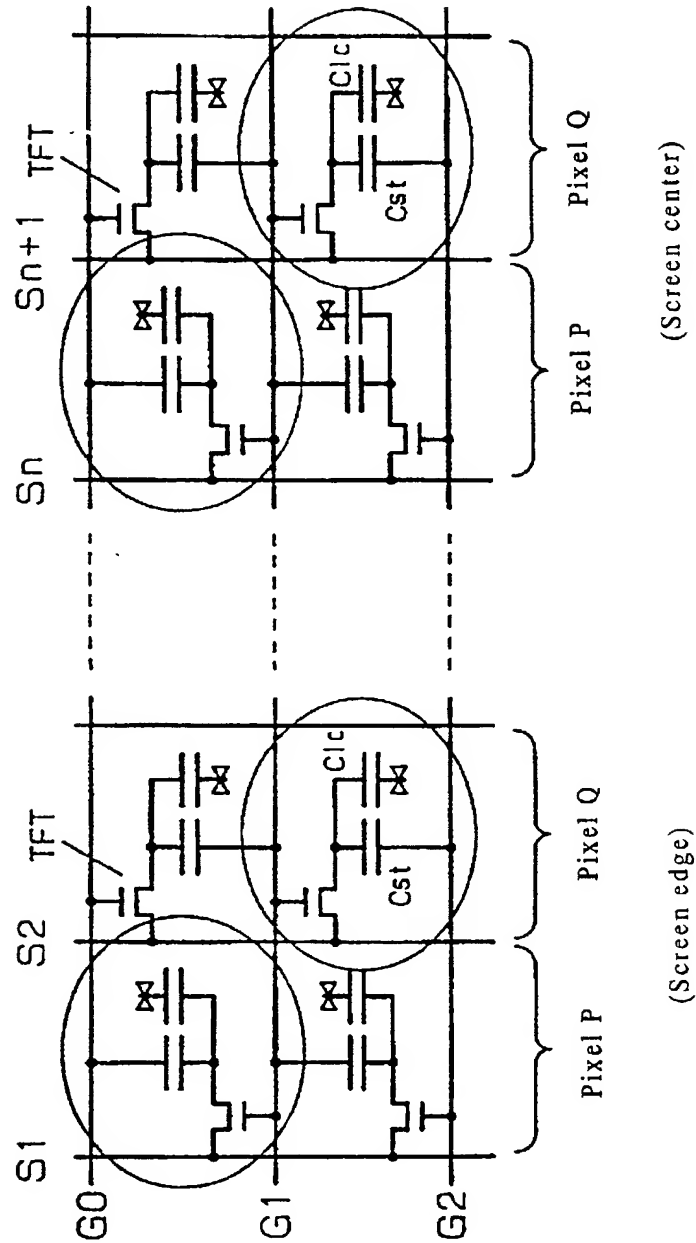


FIG. 38

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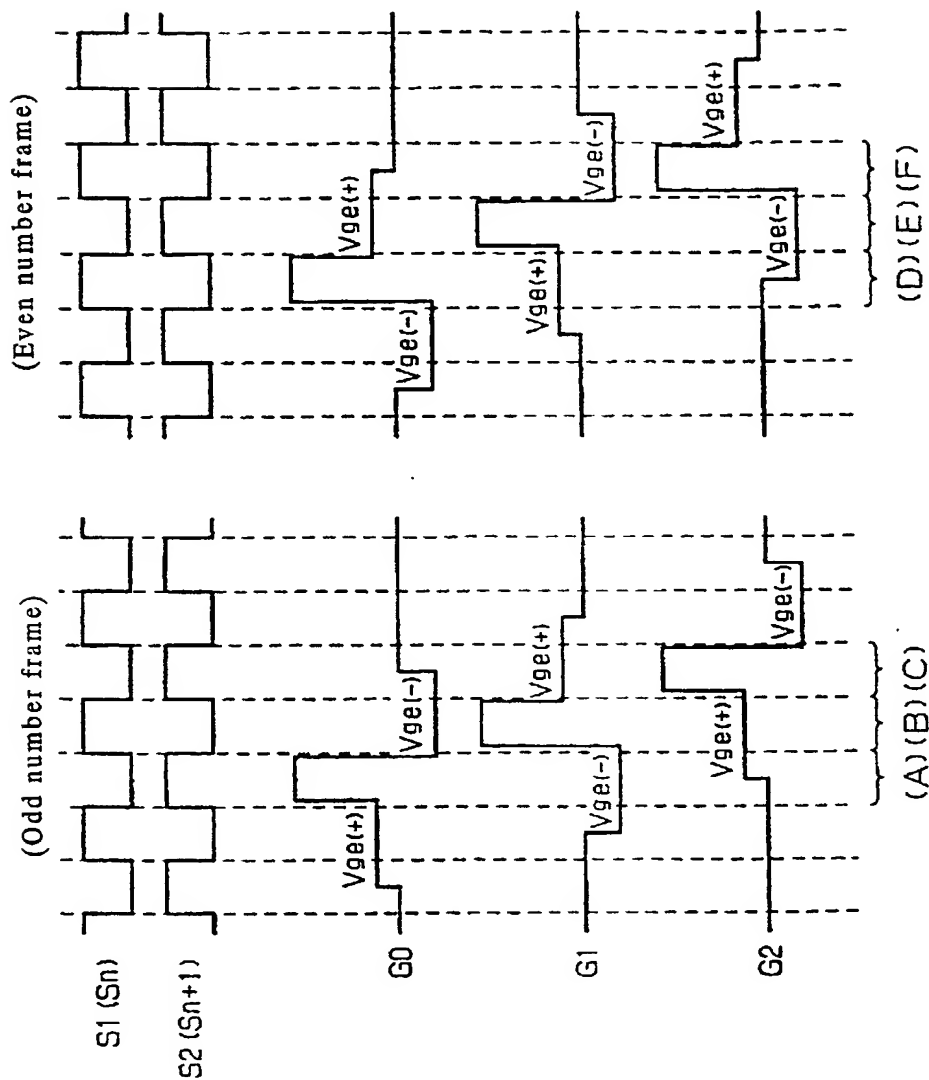


FIG. 39

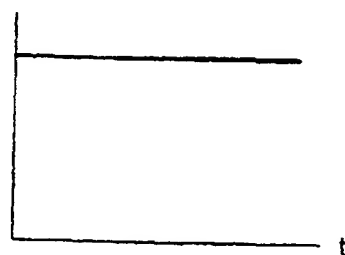
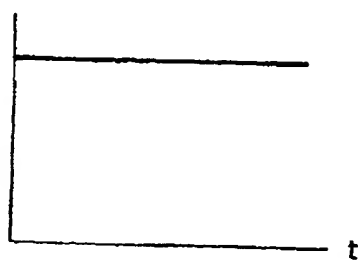
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(Power feeding edge)

(Terminal edge)

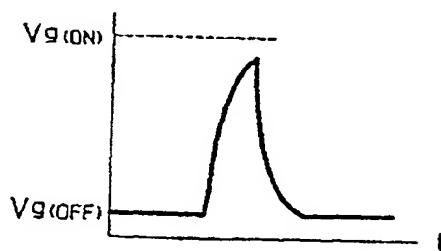
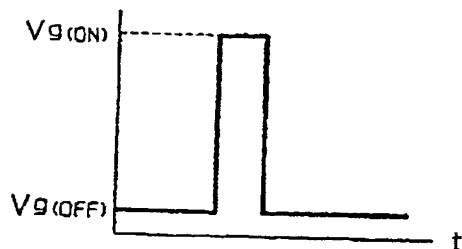
Source voltage

Source voltage



Gate voltage

Gate voltage



Pixel voltage

Pixel voltage

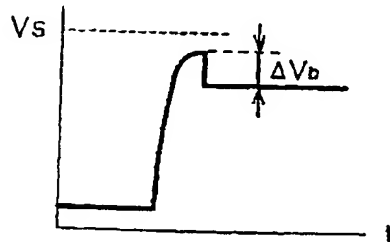
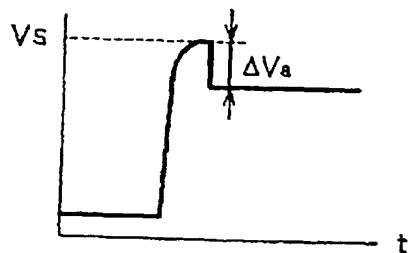


FIG. 40

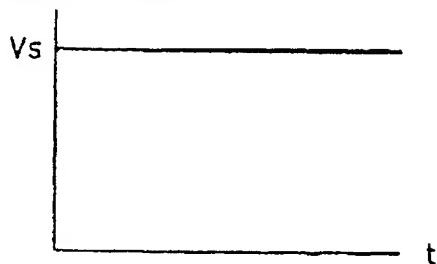


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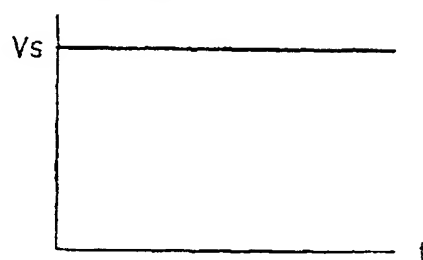
(Power feeding edge)

(Terminal edge)

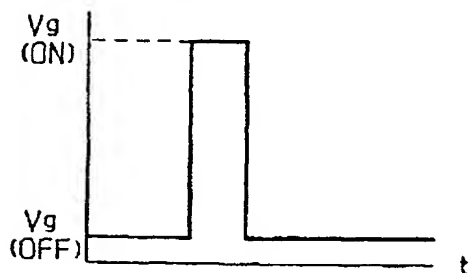
Source voltage



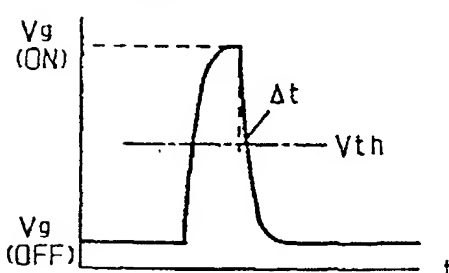
Source voltage



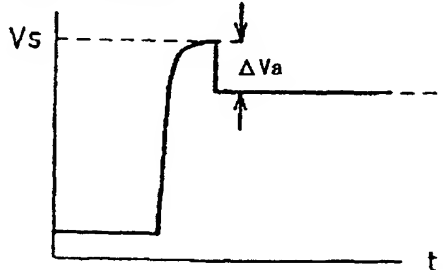
Gate voltage



Gate voltage



Pixel voltage



Pixel voltage

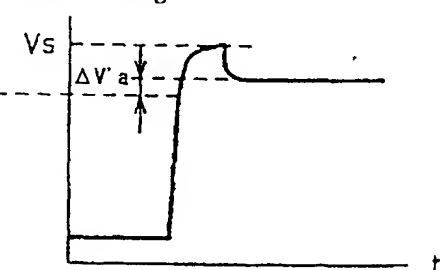


FIG. 41

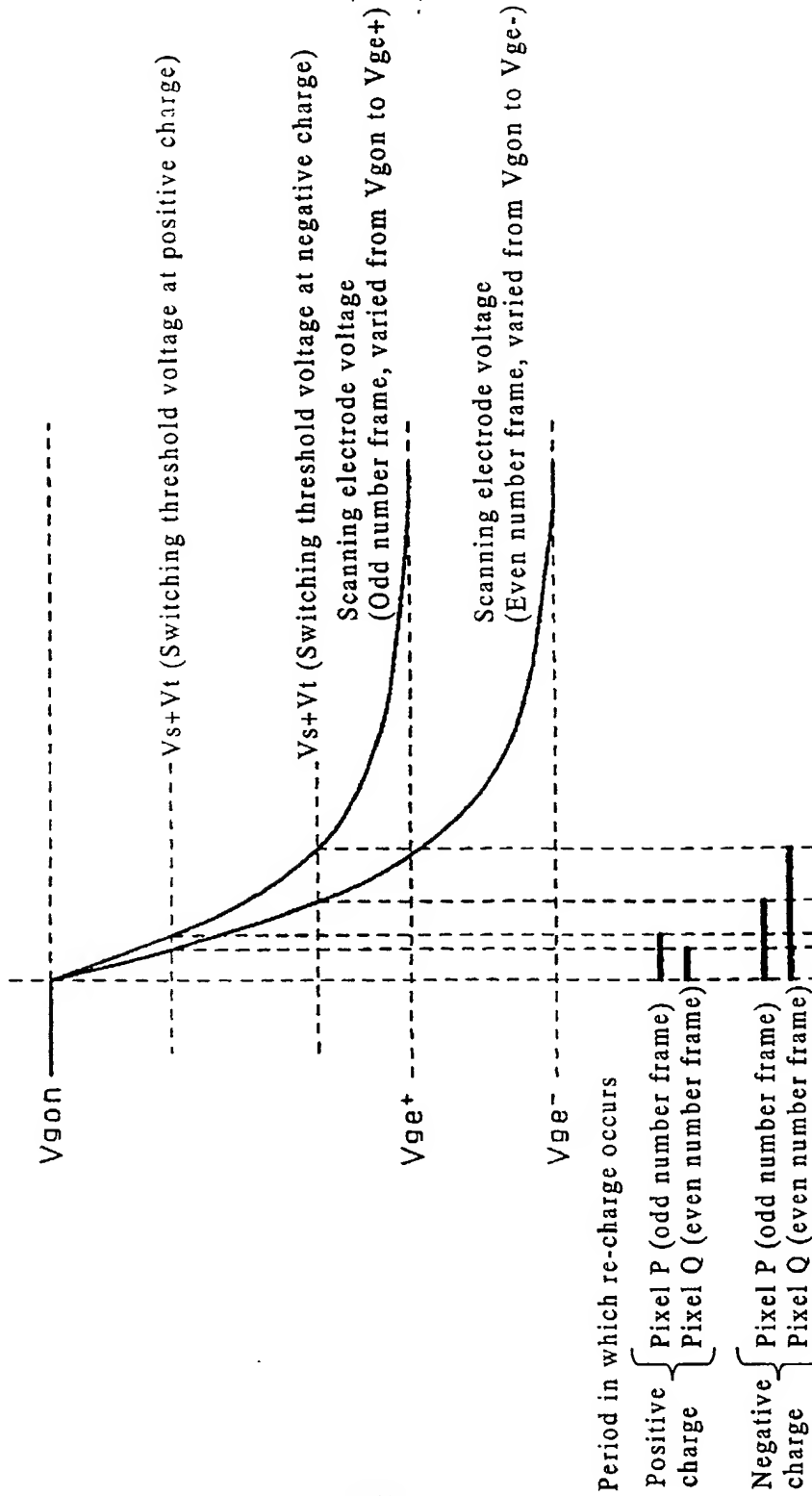


FIG. 42

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(Power feeding edge)

(Terminal edge)

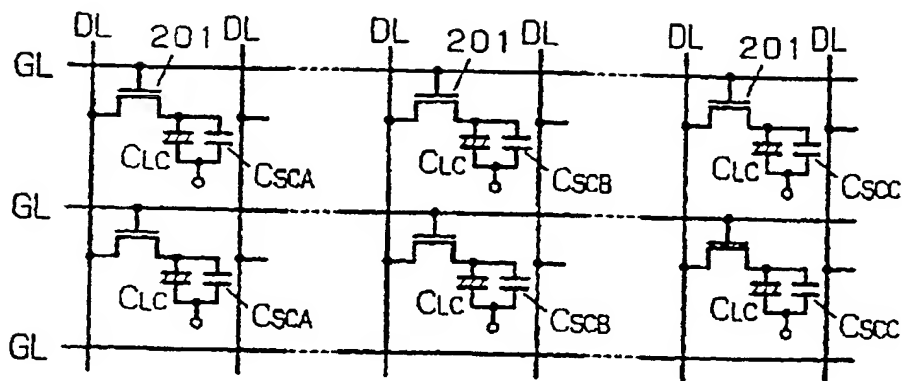


FIG. 43

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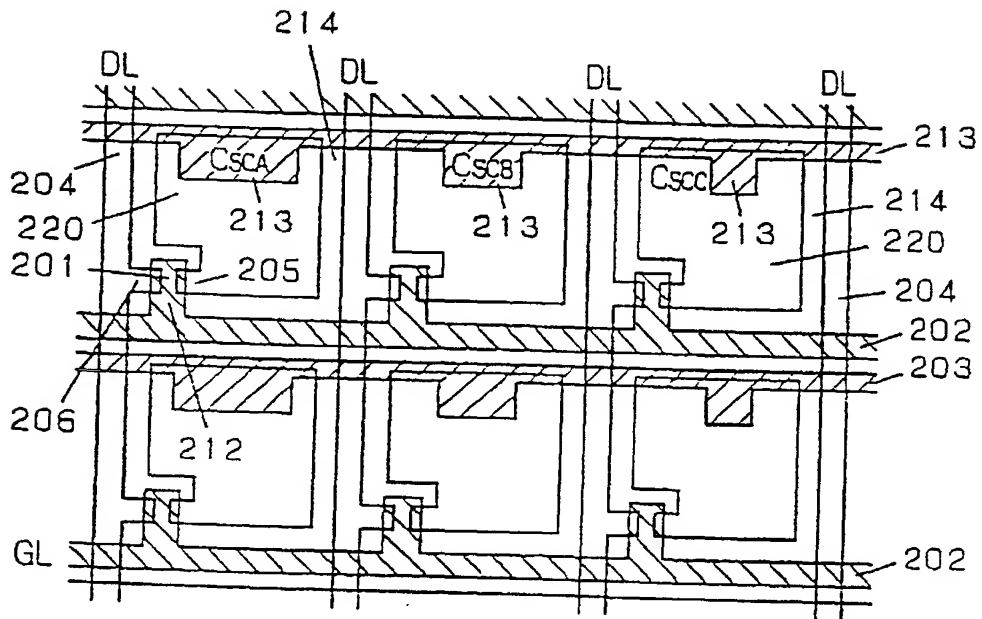


FIG. 44